Hall Ticket No	Question Paper Code: BPE203				
INSTITUTE OF AERONAUTICAL ENGINEERING					
E IARE (Autonomous)					
M.Tech I Semester End Examinations (Regular	r) - February, 2017				
Regulation: IARE–R16					
PROGRAMMABLE LOGIC CONTROLLERS AND	D THEIR APPLICATIONS				
(Power Electronics and Electrica	al Drives)				
Time: 3 Hours	Max Marks: 70				

## Answer ONE Question from each Unit All Questions Carry Equal Marks All parts of the question must be answered in one place only

## $\mathbf{UNIT}-\mathbf{I}$

1.	(a) Describe some of the main features of programmable controller.	[6M]
	(b) Describe some of the benefits on using programmable controller.	[8M]
2.	(a) Illustrate a typical ASCII transmission by transmitting character Z.	[6M]
	(b) Explain components of a modularized PLC.	[8M]

## $\mathbf{UNIT}-\mathbf{II}$

3. (a) Examine the principles of programming adopted while using Ladder Diagrams (LD) to control programmable logic controllers. [6M]

- (b) Explain the operation of the following input devices, stating the form of the signal being sensed and the output [8M]
  - i. reed switch
  - ii. incremental shaft encoder
  - iii. photoelectric transmissive switch
  - iv. diaphragm pressure switch
- 4. (a) Briefly explain the architecture of a programmable logic controller. [6M]
  - (b) Determine the memory requirements for an application with the following specifications: [8M]
     70 outputs, with each output driven by logic composed of 10 contact elements
    - -11 timers and 3 counters, each having 8 and 5 elements, respectively

-20 instructions that include addition, subtraction and comparison, each driven by 5 contact elements

Table given below provides information about the applications memory utilization requirements

Instruction	Words of memory required
Examine ON or OFF (contacts)	1
Output coil	1
Add/subtact/compare	1
Timer/counter	3

## $\mathbf{UNIT}-\mathbf{III}$

5.	(a)	Draw the ladder diagram for a T flip flop CR1 which will toggle only when IN1 and IN2 are off.	both $[7M]$			
	(b)	Explain branching and convergence in sequential function chart with an example.	[7M]			
6.	(a)	Write a sequential function chart program for traffic lamp sequence controller controlling and red light.	$\operatorname{green}$ [6 $\mathbf{M}$ ]			
	(b)	Draw the ladder rungs represented by the boolean equations i. $Q = (A.B + C) . \overline{D}.E.\overline{F}$ ii. $Q = A + \overline{B}$ iii. $Q = \overline{A}.B. \ \overline{C} + D$ i. $Q = A - \overline{B} + C - \overline{D}$	[8M]			
		iv. $Q = A.B + C.D$				
	$\mathbf{UNIT} - \mathbf{IV}$					
7.	(a)	List the various steps in commissioning programmable logic controller based system.	[7M]			
	(b)	Explain the specifications of IEC 1131 standard.	[7M]			
8.	(a)	Explain in detail about documentation of a PLC system.	[7M]			
	(b)	Explain hardware and software to implement a water level controller with pump motor cousing four switches.	[7M]			
$\mathbf{UNIT} - \mathbf{V}$						
9.	(a)	Explain how ladder diagram is created from process control descriptions.	[7M]			
	(b)	Demonstrate with an example, the hardware and software design to implement any applied of your own.	ation [ <b>7M</b> ]			

 10. (a) Explain PLC applications in detail.
 [7M]

 (b) Specify how rotor-resistance cutting method of starting induction motor is implemented using programmable logic controller.
 [7M]