

INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)

Dundigal, Hyderabad -500 043

INFORMATION TECHNOLOGY

COURSE DESCRIPTOR

Course Title	COMPILER DESIGN						
Course Code	AIT004	AIT004					
Programme	B.Tech	B.Tech					
Semester	V	CSE	E IT				
Course Type	Core						
Regulation	IARE - R16						
	Theory Practic				cal		
Course Structure	Lectur	res	Tutorials	Credits	Laboratory	Credits	
	3		1	4	-	-	
Chief Coordinator	Dr. K Srinivasa Reddy, Professor						
Course Faculty	Ch. Suresh Kumar Raju, Assistant Professor						

I. COURSE OVERVIEW:

This course deals with the basic techniques of compiler construction and tools that can be used to perform syntax-directed translation of a high-level programming language into an executable code. This will provide deeper insights into the more advanced semantics aspects of programming languages, code generation, machine independent optimizations, dynamic memory allocation, types and their inferences and object orientation.

II. COURSE PRE-REQUISITES:

Level	Course Code	Semester	Prerequisites	credits
UG	ACS001	Ι	Computer Programming	3
UG	ACS002	Π	Data Structures	4
UG	AHS013	III	Discrete Mathematical Structures	4
UG	AIT002	IV	Theory of Computation	3

III. MARKS DISTRIBUTION:

Subject	SEE Examination	CIA Examination	Total Marks
Compiler Design	70 Marks	30 Marks	100

IV. DELIVERY / INSTRUCTIONAL METHODOLOGIES:

~	Chalk & Talk	~	Quiz	~	Assignments	×	MOOCs
~	LCD / PPT	~	Seminars	×	Mini Project	~	Videos
×	✗ Open Ended Experiments						

V. EVALUATION METHODOLOGY:

The course will be evaluated for a total of 100 marks, with 30 marks for Continuous Internal Assessment (CIA) and 70 marks for Semester End Examination (SEE). Out of 30 marks allotted for CIA during the semester, marks are awarded by taking average of two CIA examinations or the marks scored in the make-up examination.

Semester End Examination (SEE): The SEE is conducted for 70 marks of 3 hours duration. The syllabus for the theory courses is divided into FIVE modules and each module carries equal weightage in terms of marks distribution. The question paper pattern is as follows. Two full questions with "either" or "choice" will be drawn from each module. Each question carries 14 marks. There could be a maximum of two sub divisions in a question.

The emphasis on the questions is broadly based on the following criteria:

50 %	To test the objectiveness of the concept.
50 %	To test the analytical skill of the concept OR to test the application skill of the concept.

Continuous Internal Assessment (CIA):

CIA is conducted for a total of 30 marks (Table 1), with 25 marks for Continuous Internal Examination (CIE), 05 marks for Quiz/Alternative Assessment Tool (AAT).

Table 1: Assessment	pattern	for	CIA
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Component		Total Marka	
Type of Assessment	CIE Exam	Quiz / AAT	
CIA Marks	25	05	30

Continuous Internal Examination (CIE):

Two CIE exams shall be conducted at the end of the 8th and 16th week of the semester respectively. The CIE exam is conducted for 25 marks of 2 hours duration consisting of two parts. Part–A shall have five compulsory questions of one mark each. In part–B, four out of five

questions have to be answered where, each question carries 5 marks. Marks are awarded by taking average of marks scored in two CIE exams.

Quiz / Alternative Assessment Tool (AAT):

Two Quiz exams shall be online examination consisting of 25 multiple choice questions and are be answered by choosing the correct answer from a given set of choices (commonly four). Marks shall be awarded considering the average of two quizzes for every course. The AAT may include seminars, assignments, term paper, open ended experiments, five minutes video and MOOCs.

	Program Outcomes (POs)	Strength	Proficiency assessed
			by
PO 1	Engineering knowledge: Apply the knowledge of	3	Assignments
	mathematics, science, engineering fundamentals, and		
	an engineering specialization to the solution of		
	complex engineering problems.		
PO 2	Problem analysis: Identify, formulate, review research	3	Seminars
	literature, and analyze complex engineering problems		
	reaching substantiated conclusions using first		
	principles of mathematics, natural sciences, and		
	engineering sciences		
PO 3	Design/development of solutions: Design solutions	3	Assignments
	for complex engineering problems and design system		
	components or processes that meet the specified needs		
	with appropriate consideration for the public health and		
	safety, and the cultural, societal, and environmental		
	considerations		
PO 4	Conduct investigations of complex problems: Use	2	Seminars
	research-based knowledge and research methods		
	including design of experiments, analysis and		
	interpretation of data, and synthesis of the information		
	to provide valid conclusions.		

VI. HOW PROGRAM OUTCOMES ARE ASSESSED:

3 = High; **2** = Medium; **1** = Low

VII. HOW PROGRAM SPECIFIC OUTCOMES ARE ASSESSED:

	Program Specific Outcomes (PSOs)	Strength	Proficiency assessed
			by
PSO 1	Professional Skills: The ability to understand, analyze	2	Assignments
	and develop computer programs in the areas related to		
	algorithms, system software, multimedia, web design,		

	Program Specific Outcomes (PSOs)	Strength	Proficiency assessed by
	big data analytics, and networking for efficient design		
	of computer-based systems of varying complexity.		
PSO 2	Problem-Solving Skills: The ability to apply standard practices and strategies in software project development using open-ended programming environments to deliver a quality product for business success.	2	Seminars
PSO 3	SuccessfulCareerandEntrepreneurship: Theabilitytoemploymoderncomputerlanguages,environments,andplatformsincreatinginnovativecareerpathstobeanentrepreneur,anda zestforhigherstudies.	-	-

3 = High; **2** = Medium; **1** = Low

VIII. COURSE OBJECTIVES :

The cours	The course should enable the students to:						
Ι	Apply the principles in the theory of computation to the various stages in the design of						
	compilers.						
II	Demonstrate the phases of the compilation process and able to describe the purpose and						
	operation of each phase.						
III	Analyze problems related to the stages in the translation process.						
IV	Exercise and reinforce prior programming knowledge with a non-trivial programming project						
	to construct a compiler.						

IX. COURSE OUTCOMES (COs):

COs	Course Outcome	CLOs	Course Learning Outcome
CO 1	Understand the various phases of compiler and	CLO 1	Define the phases of a typical compiler, including the front and backend.
	design the lexical analyzer.	CLO 2	Recognize the underlying formal models such as finite state automata, push-down automata and their connection to language definition through regular expressions and grammars.
		CLO 3	Identify tokens of a typical high-level programming language; define regular expressions for tokens and design and implement a lexical analyzer using a typical scanner generator.
CO 2	Explore the similarities and differences among	CLO 4	Explain the role of a parser in a compiler and relate the yield of a parse tree to a grammar derivation
	various parsing techniques and grammar transformation techniques	CLO 5	Apply an algorithm for a top-down or a bottom-up parser construction; construct a parser for a given context-free grammar.
		CLO 6	Demonstrate Lex tool to create a lexical analyzer and Yacc tool to create a parser.
CO 3	Analyze and implement syntax directed	CLO 7	Understand syntax directed translation schemes for a given context free grammar.
	translations schemes and intermediate code generation.	CLO 8	Implement the static semantic checking and type checking using syntax directed definition (SDD) and syntax directed translation (SDT).

COs	Course Outcome	CLOs	Course Learning Outcome			
		CLO 9	Understand the need of intermediate code generation phase in compilers.			
		CLO 10	Write intermediate code for statements like assignment, conditional, loops and functions in high level language.			
		CLO 11	Explain the role of a semantic analyzer and type checking; create a syntax-directed definition and an annotated parse tree; describe the purpose of a syntax tree.			
		CLO 12	Design syntax directed translation schemes for a given context free grammar.			
CO 4 Describe the concepts of type checking and analyze runtime allocation		CLO 13 Explain the role of different types of runti environments and memory organization for implementation of programming languages				
	strategies.	CLO 14	Differentiate static vs. dynamic storage allocation and the usage of activation records to manage program modules and their data.			
		CLO 15	Understand the role of symbol table data structure in the construction of compiler.			
CO 5	Demonstrate the algorithms to perform code optimization and	CLO 16	Learn the code optimization techniques to improve the performance of a program in terms of speed & space.			
	code generation.	CLO 17	Implement the global optimization using data flow analysis such as basic blocks and DAG.			
		CLO 18	Understand the code generation techniques to generate target code.			
		CLO 19	Design and implement a small compiler using a software engineering approach.			
		CLO 20	Apply the optimization techniques to intermediate code and generate machine code			

X. COURSE LEARNING OUTCOMES (CLOs):

CLO	CLO's	At the end of the course, the student will have	PO's	Strength of
Code		the ability to:	Mapped	Mapping
AIT004.01	CLO 1	Define the phases of a typical compiler, including	PO 1,	3
		the front and backend.	PO 2	
AIT004.02	CLO 2	Recognize the underlying formal models such as	PO 1,	3
		finite state automata, push-down automata and	PO 4	
		their connection to language definition through		
		regular expressions and grammars.		
AIT004.03	CLO 3	Identify tokens of a typical high-level	PO 3	3
		programming language; define regular expressions		
		for tokens and design and implement a lexical		
		analyzer using a typical scanner generator.		
AIT004.04	CLO 4	Explain the role of a parser in a compiler and relate	PO 1,	3
		the yield of a parse tree to a grammar derivation.	PO 2	
AIT004.05	CLO 5	Apply an algorithm for a top-down or a bottom-up	PO 2	2
		parser construction; construct a parser for a		
		given context-free grammar.		
AIT004.06	CLO 6	Demonstrate Lex tool to create a lexical analyzer	PO 1,	3
		and Yacc tool to create a parser.	PO 4	
AIT004.07	CLO 7	Understand syntax directed translation schemes for	PO 1,	3
		a given context free grammar.	PO 4	
AIT004.08	CLO 8	Implement the static semantic checking and type	PO 1,	3
		checking using syntax directed definition	PO 2	
		(SDD) and syntax directed translation (SDT).		

CLO	CLO's	At the end of the course, the student will have	PO's	Strength of
Code		the ability to:	Mapped	Mapping
AIT004.09	CLO 9	Understand the need of intermediate code	PO 3,	3
		generation phase in compilers.	PO 4	
AIT004.10	CLO 10	Write intermediate code for statements like	PO 1,	3
		assignment, conditional, loops and functions in	PO 4	
		high level language.		
AIT004.11	CLO 11	Explain the role of a semantic analyzer and type	PO 4	2
		checking; create a syntax-directed definition		
		and an annotated parse tree; describe the purpose of		
		a syntax tree.		
AIT004.12	CLO 12	Design syntax directed translation schemes for a	PO 1,	3
		given context free grammar.	PO 3	
AIT004.13	CLO 13	Explain the role of different types of runtime	PO 1	2
		environments and memory organization for		
		implementation of programming languages.		
AIT004.14	CLO 14	Differentiate static vs. dynamic storage allocation	PO 2	3
		and the usage of activation records to manage		
		program modules and their data.		
AIT004.15	CLO 15	Understand the role of symbol table data structure	PO 1	2
		in the construction of compiler.		
AIT004.16	CLO 16	Learn the code optimization techniques to improve	PO 2	3
		the performance of a program in terms of speed &		
		space.		
AIT004.17	CLO 17	Implement the global optimization using data flow	PO 1	2
		analysis such as basic blocks and DAG.		
AIT004.18	CLO 18	Understand the code generation techniques to	PO 3	3
		generate target code.		
AIT004.19	CLO 19	Design and implement a small compiler using a	PO 1,	3
		software engineering approach.	PO 3	
AIT004.20	CLO 20	Apply the optimization techniques to intermediate	PO 1,	3
		code and generate machine code	PO 4	

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XI. MAPPING COURSE OUTCOMES LEADING TO THE ACHIEVEMENT OF PROGRAM OUTCOMES AND PROGRAM SPECIFIC OUTCOMES:

Course		Program C	Outcomes (P	Program Specific Outcomes (PSOs)		
(COs)	PO1	PO2	PO3	PO4	PSO1	PSO2
CO 1	3	2	3	2	3	3
CO 2	3	3		3	3	3
CO 3	3	3	3	3	2	3
CO 4	2	3			2	2
CO 5	3	3	3	2	2	3

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XII. MAPPING COURSE LEARNING OUTCOMES LEADING TO THE ACHIEVEMENT OF PROGRAM OUTCOMES AND PROGRAM SPECIFIC OUTCOMES:

Course Learning	Program Outcomes (POs)											Program Specific Outcomes (PSOs)			
Outcomes (CLOs)	PO1	PO1 PO2 PO3 PO4 PO5 PO6 PO7 PO8 PO9 PO10 PO11 PO12									PSO1	PSO2	PSO3		
CLO 1	3	2											3	2	

Course Learning	Program Outcomes (POs)											Program Specific Outcomes (PSOs)			
Outcomes (CLOs)	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3
CLO 2	3			2									2		
CLO 3			3												
CLO 4	2	3											2		
CLO 5		2												3	
CLO 6	3			2									3		
CLO 7	2			3									2		
CLO 8	2	3													
CLO 9			3	3									2	3	
CLO 10	3			2											
CLO 11				2									3		
CLO 12	3		3										2		
CLO 13	2													2	
CLO 14		3											2		
CLO 15	2													2	
CLO 16		3											2		
CLO 17	2													2	
CLO 18			3												
CLO 19	3		3										2		
CLO 20	3			2									2	3	

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XIII. ASSESSMENT METHODOLOGIES – DIRECT

CIE Exams	PO1, PO2, PO3,PO4, PSO1,PSO2	SEE Exams	PO1, PO2, PO3,PO4, PSO1,PSO2	Assignments	PO1,PO3, PSO1	Seminars	PO2,PO4, PSO2
Laboratory Practices	-	Student Viva	-	Mini Project	-	Certification	-
Term Paper	PO1, PO2, PO3,PO4, PSO1,PSO2						

XIV. ASSESSMENT METHODOLOGIES - INDIRECT

~	Early Semester Feedback	~	End Semester OBE Feedback
×	Assessment of Mini Projects by Experts		

XV. SYLLABUS

UNIT-I	INTRODUCTION TO COMPILERS AND PARSING								
Introduction to compiler, role automata, pas Parsing, role of recursion, left down parsing:	Introduction to compilers: Definition of compiler, interpreter and its differences, the phases of a compiler, role of lexical analyzer, regular expressions, finite automata, from regular expressions to finite automata, pass and phases of translation, bootstrapping, LEX-lexical analyzer generator; Parsing: Parsing, role of parser, context free grammar, derivations, parse trees, ambiguity, elimination of left recursion, left factoring, eliminating ambiguity from dangling-else grammar, classes of parsing, top-down parsing: backtracking, recursive-descent parsing, predictive parsers, LL(1) grammars.								
UNIT-II	BOTTOM-UP PARSING								
Bottom-up par shift- reduce canonical LR YACC-automa	rsing: Definition of bottom-up parsing, handles, handle pruning, stack implementation of parsing, conflicts during shift-reduce parsing, LR grammars, LR parsers-simple LR, and Look Ahead LR parsers, error recovery in parsing, parsing ambiguous grammars, atic parser generator.								
UNIT-III	SYNTAX-DIRECTED TRANSLATION AND INTERMEDIATE CODE GENERATION								
Syntax-directe Lattributed def Intermediate c notation and directed transl flow-of contro	d translation: Syntax directed definition, construction of syntax trees, S-attributed and finitions, translation schemes, emitting a translation. code generation: Intermediate forms of source programs– abstract syntax tree, polish three address code, types of three address statements and its implementation, syntax ation into three-address code, translation of simple statements, Boolean expressions and I statements								
UNIT-IV	TYPE CHECKING AND RUN TIME ENVIRONMENT								
Type checking of types, spec overloading organization, s and language f	: Definition of type checking, type expressions, type systems, static and dynamic checking ification of a simple type checker, equivalence of type expressions, type conversions, of functions and operators; Run time environments: Source language issues, Storage torage- allocation strategies, access to nonlocal names, parameter passing, symbol tables, accelities for dynamic storage allocation.								
UNIT-V	CODE OPTIMIZATION AND CODE GENERATOR								
Code optimiza graphs, peeph machine, runti allocation and	tion: The principle sources of optimization, optimization of basic blocks, loops in flow ole optimization; Code generator: Issues in the design of a code generator, the target me storage management, basic blocks and flow graphs, a simple code generator, register assignment, DAG representation of basic blocks.								
Text Books:									
1. Alfred V. Pearson Ed	Aho, Ravi Sethi, Jeffrey D. Ullman, —Compilers–Principles, Techniques and Toolsl, ucation, Low Price Edition, 2004								
Reference Bo	oks:								
1. Kenneth C 1 st Edition	 Kenneth C. Louden, Thomson, —Compiler Construction– Principles and Practicel, PWS Publishing 1st Edition, 1997 								
2. Andrew W Edition, 20 3. Richard A Engineers	2004. 2004. 2004. 2004. rnold Johnson, Irwin Miller and John E. Freund, "Probability and Statistics for 2017. Prentice Hall, 8 th Edition, 2013.								

XVI. COURSE PLAN:

The course plan is meant as a guideline. Probably there may be changes.

Lecture	Topics to be covered	Course	Reference
No		Learning Outcomes	
1.4	Introduction Analysis of the source program Difference of	(CLOs)	T1.1 1 1 5
1-4	compiler and interpreter, Phases of compilation, Grouping of phases, role of lexical analyzer.	CLO I	R1:1.1
5-6	Construction of regular grammar from regular expression, NFA,DFA.	CLO 2	T1: 3.6-3.7 R1:2.2-2.4
7	Concept of pass and difference between pass and phase.	CLO 1	T1: 1.5
8	Bootstrapping and types of compiler.	CLO 3	T1: 1.1 R1:1.6
9-11	Lex-Lexical analyzer generator, Derivations and parse tree, regular expressions v/s context free grammar.	CLO 6	T1: 3.8-4.3 R1:3.1-3.3
12-15	Backtracking, eliminating ambiguity from dangling-else grammar, Elimination of left recursion and left factoring, Recursive decent parsing, Finding FIRST and FOLLOW.	CLO 4	T1: 4.3-4.4 R1:4.1
16-18	Construction of parse tables, Predictive parsing, LL(1) grammar.	CLO 4	T1: 4.5-4.7 R1:4.3-4.5
19-21	Handles, handle pruning, Shift reduce parsing, Conflicts during shift-reduce parsing, LR parsers- Goto and closure functions.	CLO 5	T1: 4.5-4.7 R1:5.1-5.2
22-24	LR(0) and SLR and construction of parser table for SLR.	CLO 5	T1: 4.7 R1:5.3
25-27	CLR operations and construction of parser table for LALR., LALR operations and construction of parser table for LALR.	CLO 5	T1: 4.7 R1:5.4-5.5
28	Description of error recovery.	CLO 11	T1: 4.7 R1:5.6
29	Yacc parser generator.	CLO 6	T1: 4.9 R1:5.5
30	Abstract syntax tree, three address code.	CLO 9	T1: 4.9
31-32	Introduction to attributes grammars, Syntax directed definitions, applications of SDD, Implementing L-attributed SDD's.	CLO 8	T1: 5.1-5.4 R1:6.1
33	Control flow, back patching, translation of simple statements, Boolean expressions.	CLO 10	T1:8.4-8.6
34-35	Type checking, type expressions, type systems, Type conversions, Overloading.	CLO 11	T1: 6.1 R1:6.4-6.5
36-37	Source language issues, Storage organization, storage-allocation strategies. Access to nonlocal names, parameter passing.	CLO 14	T1: 7.1-7.5 R1:7.1
38-39	Symbol tables, and language facilities for dynamic storage allocation.	CLO 15	T1: 7.6-7.7
40	Principle sources of optimization.	CLO 16	T1: 10.2
41-47	Optimization of basic blocks - Local, global and scope optimization, Loops in flow graphs, peephole optimization.	CLO 17	T1:10.1-10.2 T1: 10.4,9.9
48-49	Introduction, issues in code generation, , the target machine.	CLO 18	T1: 9.1-9.2
50	Runtime storage management.	CLO 13	T1: 9.3 R1:7.6
51-52	Basic blocks and flow graphs.	CLO 17	T1: 9.4
53-54	A simple code generator, register allocation and assignment.	CLO 20	T1: 9.6-9.7 R1:8.1-8.8
55	DAG construction, applications.	CLO17	T1: 9.8

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S NO	Description	Proposed Actions	Relevance With POS	Relevance With PSOS
1	ANother Tool for Language Recognition (ANTLR)	Seminars / Guest Lectures	PO1, PO3, PO4	PSO 1
2	Java Compiler Compiler(JAVACC)	Seminars / Guest Lectures	PO 1, PO 3,PO4	PSO 3
3	Familiarization Lexer and Parser Tools	Seminars	PO 1	PSO 1
4	Awareness on Computer Architecture for fine tuning Target Codes	Seminars / NPTEL Video Lectures/ Moocs	PO 3	PSO 2

XVII. GAPS IN THE SYLLABUS-TO MEET INDUSTRY / PROFESSION REQUIREMENTS:

Prepared by: Ms. E Uma Shankari, Assistant Professor

HOD, IT