## MICROPROCESSORS AND MICROCONTROLLERS

| VI Semester: ECE    |                      |                                     |   |   |         |               |     |       |  |
|---------------------|----------------------|-------------------------------------|---|---|---------|---------------|-----|-------|--|
| Course Code         | Category             | Hours / Week                        |   |   | Credits | Maximum Marks |     |       |  |
| AECB24              | CORE                 | L                                   | Т | Р | С       | CIA           | SEE | Total |  |
|                     |                      | 2                                   | 1 | - | 3       | 30            | 70  | 100   |  |
| Contact Classes: 30 | Tutorial Classes: 15 | Practical Classes: Nil Total Classe |   |   |         | s: 45         |     |       |  |

## **OBJECTIVES:**

## The students will try to learn:

- I Signal descriptions with functional architecture and hardware interfacing skills with microprocessors and microcontrollers.
- II The instruction set and logic to build assembly language programs for automated electronic systems
- III The essential concepts of development through a practical, hands-on approach on advanced processors such as ARM processors and Internet of Things based systems.

## **COURSE OUTCOMES:**

After successful completion of the course, Students will be able to

- CO 1 **Describe** the features of Intel processors and microcontrollers for signal description **and** architecture.
- CO 2 Illustrate instruction set for efficient assembly language level programming.
- CO 3 **Construct the** maximum and stand-alone modes of operation using circuit schematic & timing diagrams.
- CO 4 Select appropriate address mapping and hardware design for volatile and non-volatile memories.
- CO 5 **Illustrate** interfacing devices with microprocessor and microcontroller using Programmable Peripheral Interface (PPI) and Interrupt Controllers.
- CO 6 **Outline** the working of stepper motor for required number of rotations clockwise & anticlockwise.
- CO 7 Analyse the benefits of direct memory access using DMA controllers with necessary hardware and software.
- CO 8 Compare synchronous & asynchronous communication schemes using appropriate resources of microprocessor and microcontrollers
- CO 9 Outline minimization of hardware while using internal resources of Micro controller.
- CO 10 Describe key de-bouncing to take care of fluctuations while using keyboard- display units.
- CO 11 **Build** prototype models and products subsequently in embedded field for real life needs and applications.
- MODULE -I 8086 MICROPROCESSORS

Classes: 08

Register organization of 8086, Architecture, signal description of 8086, physical memory organization, general bus operation, I/O addressing capability, special purpose activities, Minimum mode, maximum mode of 8086 system and timings, machine language instruction formats, addressing mode of 8086, instruction set off 8086, assembler directives and operators.

|   | PROGRAMMING WITH 8086 MICROPROCESSOR   | Classes: 09                                  |
|---|--|--|
| stack structure of  | rograms, programming with an assembler, Assembly language programs, intro-<br>of 8086/8088, interrupts and interrupt service routines. Interrupt cycle of 8086<br>rrupt and mask able interrupts, interrupt programming.   |  |
| MODULE -III   | INTERFACING WITH 8086/88   | Classes: 08                                  |
|   | memory interfacing, dynamic RAM interfacing, interfacing i/o ports, PIO 55, interfacing to D/A and A/D converters, stepper motor interfacing, contro 255.  |  |
| Programmable communication  | interrupt controller 8259A, the keyboard /display controller8279, interface 8251 USART, DMA Controller 8257.   | programmable                                 |
| MODULE -IV  | 8051 MICROCONTROLLER   | Classes: 10                                  |
|   | roller – Internal architecture and pin configuration, 8051 addressing modes, inst<br>ures. I/O Port structures, assembly language programming using data transfer, a<br>ch instructions.   |  |
| MODULE -V   | SYSTEM DESIGN USING MICROCONTROLLER  | Classes: 10                                  |
| table, Interrupt j  | ounters, Serial data communication and its programming, 8051 interrupts, Interrupts, or gramming. Real world interfacing of 8051 with external memory, expansion C, stepper motor interfacing.   |  |
| <b>Text Books:</b>  |  |  |
|   |  |  |
| 2. Muhamm<br>using Ass  | , Bhurchandi K.M, "Advanced Microprocessor and Peripherals", TMH, 2nd Edit<br>ad Ali Mazidi, J.G. Mazidi, R.D McKinlay," The 8051 Microcontroller and Em<br>embly and C", Pearson education, 2nd Edition, 2009.<br><i>J</i> . Hall, "Microprocessors and Interfacing Programming and Hardware", TMGH   | bedded systems                               |
| <ol> <li>Muhamm<br/>using Ass</li> <li>Douglas V<br/>1994.</li> </ol>   | ad Ali Mazidi, J.G. Mazidi, R.D McKinlay," The 8051 Microcontroller and Emembly and C", Pearson education, 2nd Edition, 2009.<br>J. Hall, "Microprocessors and Interfacing Programming and Hardware", TMGH   | bedded systems                               |
| <ol> <li>Muhamm<br/>using Ass</li> <li>Douglas V<br/>1994.</li> <li>Reference Bool</li> <li>Kenneth</li> </ol>  | ad Ali Mazidi, J.G. Mazidi, R.D McKinlay," The 8051 Microcontroller and Emembly and C", Pearson education, 2nd Edition, 2009.<br>J. Hall, "Microprocessors and Interfacing Programming and Hardware", TMGH   | bedded systems<br>I,2 <sup>nd</sup> Edition, |
| <ol> <li>Muhamm<br/>using Ass</li> <li>Douglas V<br/>1994.</li> <li>Reference Bool</li> <li>Kenneth</li> <li>Manish K</li> </ol>  | ad Ali Mazidi, J.G. Mazidi, R.D McKinlay," The 8051 Microcontroller and Emembly and C", Pearson education, 2nd Edition, 2009.<br>/. Hall, "Microprocessors and Interfacing Programming and Hardware", TMGH<br>ks:<br>J. Ayala, "The 8051 Microcontroller", Thomson Learning, 3rd edition, 2005.<br>. Patel, "The 8051 Microcontroller Based Embedded Systems", McGraw Hill, 1  | bedded systems<br>I,2 <sup>nd</sup> Edition, |
| <ol> <li>Muhamm<br/>using Ass</li> <li>Douglas V<br/>1994.</li> <li>Reference Bool</li> <li>Kenneth</li> <li>Manish K</li> <li>Web Reference</li> <li>http://ww</li> <li>http://ww</li> </ol> | ad Ali Mazidi, J.G. Mazidi, R.D McKinlay," The 8051 Microcontroller and Emembly and C", Pearson education, 2nd Edition, 2009.<br>/. Hall, "Microprocessors and Interfacing Programming and Hardware", TMGH<br>ks:<br>J. Ayala, "The 8051 Microcontroller", Thomson Learning, 3rd edition, 2005.<br>. Patel, "The 8051 Microcontroller Based Embedded Systems", McGraw Hill, 1  | bedded systems<br>I,2 <sup>nd</sup> Edition, |
| <ol> <li>Muhamm<br/>using Ass</li> <li>Douglas V<br/>1994.</li> <li>Reference Bool</li> <li>Kenneth</li> <li>Manish K</li> <li>Web Reference</li> <li>http://ww</li> <li>http://ww</li> </ol> | ad Ali Mazidi, J.G. Mazidi, R.D McKinlay," The 8051 Microcontroller and Emembly and C", Pearson education, 2nd Edition, 2009.<br>/. Hall, "Microprocessors and Interfacing Programming and Hardware", TMGH<br>ks:<br>J. Ayala, "The 8051 Microcontroller", Thomson Learning, 3rd edition, 2005.<br>. Patel, "The 8051 Microcontroller Based Embedded Systems", McGraw Hill, 1<br>s:<br>/w.nptel.ac.in/downloads/106108100/<br>w.the8051 microcontroller.com/web-references | bedded systems<br>I,2 <sup>nd</sup> Edition, |