

#### **INSTITUTE OF AERONAUTICAL ENGINEERING**

(Autonomous) Dundigal, Hyderabad - 500043, Telangana

# COMPUTER SCIENCE AND ENGINEERING (AI & ML)

### ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT

Name of the faculty:

Mr. J SIVA RAMAKRISHNA

Department:

Computer Science and Engineering (AI & ML)

Regulation:

IARE - R20

Batch:

2020-2024

Course Name:

**Analog and Digital Electronics** 

Course Code:

AECC08

Semester:

Target Value:

60% (1.8)

#### Attainment of COs:

Course Outcome		Direct Attainment	Indirect Attainment	Overall Attainment	Observation
CO1	Demonstrate the volt-ampere characteristics of semiconductor devices for finding cut-in voltage, resistance and capacitance.	0.90	2.30	1.2	Not Attained
CO2	Illustrate half wave and full wave rectifier circuits with filter and without filters used to convert the alternating current in to direct current.	0.90	2.30	1.2	Not Attained
CO3	Analyze the input and output characteristics of transistor configurations and small signal h-parameter models to determine the input - output resistances, current gain and voltage gain	0.90	2.20	1.2	Not Attained
CO4	Identify the functionality of logic gates, parity code and hamming code techniques for error detection and correction of single bit in digital systems.	0.60	2.30	0.9	Not Attained
CO5	Make use of appropriate logic gates to implement combinational logic circuits.	0.30	2.30	0.7	Not Attained
CO6	Select a required flip flop to realize synchronous and asynchronous counters for memory storing applications.	0.90	2.30	1.2	Not Attained

## Action Taken Report: (To be filled by the concerned faculty / course coordinator)

CO1: We would plan to make students get more practice on semiconductor devices for finding cut-in, voltage

CO2: We would plan to conduct workshop on full wave rectifier circuits

CO3: We will paln to take extra classes on transistor configurations

CO4: We would plan to make students get more practice on error detection.

CO5: We would conduct workshop on logic gates to implement combinational logic circuits

CO6: We would plant to take extra classes on flip flops.

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