


**INSTITUTE OF AERONAUTICAL ENGINEERING**

(Autonomous)

Dundigal, Hyderabad - 500043, Telangana

**COMPUTER SCIENCE AND ENGINEERING (AI & ML)**
**ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT**

Name of the faculty:	<b>Dr. B RAVI KUMAR</b>	Department:	<b>Computer Science and Engineering (AI &amp; ML)</b>
Regulation:	<b>IARE - R20</b>	Batch:	<b>2021-2025</b>
Course Name:	<b>Analog and Digital Electronics</b>	Course Code:	<b>AECC08</b>
Semester:	<b>III</b>	Target Value:	<b>60% (1.8)</b>

**Attainment of COs:**

	<b>Course Outcome</b>	<b>Direct Attainment</b>	<b>Indirect Attainment</b>	<b>Overall Attainment</b>	<b>Observation</b>
CO1	Demonstrate the volt-ampere characteristics of semiconductor devices for finding cut-in voltage, resistance and capacitance.	3.00	2.30	2.9	Attained
CO2	Illustrate half wave and full wave rectifier circuits with filter and without filters used to convert the alternating current in to direct current.	2.30	2.30	2.3	Attained
CO3	Analyze the input and output characteristics of transistor configurations and small signal h-parameter models to determine the input - output resistances, current gain and voltage gain	3.00	2.20	2.8	Attained
CO4	Identify the functionality of logic gates, parity code and hamming code techniques for error detection and correction of single bit in digital systems.	2.30	2.30	2.3	Attained
CO5	Make use of appropriate logic gates to implement combinational logic circuits.	2.30	2.30	2.3	Attained
CO6	Select a required flip flop to realize synchronous and asynchronous counters for memory storing applications.	2.00	2.30	2.1	Attained

**Action Taken Report: (To be filled by the concerned faculty / course coordinator)**
**Course Coordinator**
**Mentor**
**Head of the Department**

**Head of the Department**  
 Artificial Intelligence & Machine Learning  
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