

Dundigal, Hyderabad - 500043, Telangana

## **CIVIL ENGINEERING**

## ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT

Name of the faculty:

Mr. P SANDEEP KUMAR

Department:

**Civil Engineering** 

Regulation:

IARE - R18

Batch:

2018-2022

Course Name:

**BASIC ELECTRONICS ENGINEERING** 

Course Code:

AECB01

Semester:

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Target Value:

60% (1.8)

## **Attainment of COs:**

	Course Outcome	Direct attaiment	Indirect attaiment	Overall attaiment	Observation
CO1	Demonstrate the properties of semiconductor materials which forms the basis for the formation of pn junction diode and zener diode	2.70	2.30	2.6	Attained
CO2	Extend the pn junction characteristics for the diode applications such as switch and rectifiers	1.00	2.20	1.2	Not Attained
со3	Utilize the inverting and non-inverting amplifiers as arithmetic operations, waveform generator and in IC related real time applications	0.00	2.20	0.4	. Not Attained
CO4	Extend the different modes of op-amp configurations for finding parameters of slew rate, CMRR and PSRR	0.00	2.60	0.5	Not Attained
CO5	Identify the different performance characteristics and specifications of data converters	0.70	2.50	1.1	Not Attained
CO6	Interpret the need of sequential logic design principles for designing flip-flops, counters and shift registers.	0.00	2.30	0.5	Not Attained

## Action taken report: CO2: Additional inputs will be provided on extending the junction characteristics for the diode applications such as switch and rectifiers CO3: Giving assignments and conducting tutorials on utilize the inverting and non-inverting amplifiers as arithmetic operations, waveform generator and in IC related real time applications CO4: Provide more problems and assignments on extend the different modes of op-amp configurations for finding parameters of slew rate, CMRR and PSRR CO5: Providing more information and assignments on concepts of identify the different performance characteristics and specifications of data converters CO6: Conducting guest lectures on interpret the need of sequential logic design principles for designing flip- flops, counters and shift registers.

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