



# INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)

Dundigal, Hyderabad - 500043, Telangana

## CIVIL ENGINEERING

### ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT

Name of the faculty:	Mr. G VISWANATH	Department:	Civil Engineering
Regulation:	IARE - BT23	Batch:	2024-2028
Course Name:	Elements of Electrical and Electronics Engineering	Course Code:	AEED01
Semester:	I	Target Value:	60% (1.8)

#### Attainment of COs:

Course Outcome	Direct Attainment	Indirect Attainment	Overall Attainment	Observation
CO1 Make use of basic electrical laws for solving DC and AC circuits.	3.00	2.60	2.9	Attained
CO2 Solve the network theorems to calculate the parameters in electrical circuits.	2.00	2.50	2.1	Attained
CO3 Demonstrate the fundamentals of electromagnetism for the operation of DC and AC machines.	1.20	2.50	1.5	Not Attained
CO4 Utilize the characteristics of diodes for the construction of rectifiers and regulators circuits.	3.00	2.50	2.9	Attained
CO5 Interpret the transistor configurations for optimization of the operating point.	2.00	2.60	2.1	Attained
CO6 Illustrate the amplifier circuits using transistors for computing hybrid parameters.	0.80	2.50	1.1	Not Attained

#### Action Taken Report: (To be filled by the concerned faculty / course coordinator)

CO3: Provided hands-on exercises where students calculated flux, EMF, torque, and induced currents in various machine configurations.

CO6: Conducted practical lab sessions where students built common-emitter, common-collector, and common-base amplifier circuits using BJTs.

  
Course Coordinator

  
Mentor

  
Head of the Department  
Head of the Department  
Civil Engineering  
INSTITUTE OF AERONAUTICAL ENGINEERING  
Dundigal, Hyderabad - 500043