



INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)

Dundigal, Hyderabad - 500 043

COMPUTER SCIENCE AND ENGINEERING (CYBER SECURITY)

ATTAINMENT OF COURSE OUTCOME – ACTION TAKEN REPORT

Name of the faculty:	A. LAVANYA	Department:	CSE(CS)
Regulation:	IARE – UG20	Batch:	2020- 2024
Course Name:	Analog and Digital Electronics	Course Code:	AECC08
Semester:	III	Target Value:	60% (1.8)

Attainment of COs:



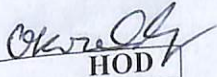
Course Outcome		Direct attainment	Indirect attainment	Overall attainment	Observation
CO1	Illustrate the volt-ampere characteristics of semiconductor devices for finding cut-in voltage, resistance and capacitance.	3	2.2	2.8	Attainment target reached
CO2	Explain half wave and full wave rectifier circuits with filter and without filters for conversion of alternating current in to direct current.	2.3	2.3	2.3	Attainment target reached
CO3	Analyze the input and output characteristics of transistor configurations and small signal h-parameter models for determining the input - output resistances, current gain and voltage gain.	0.9	2.2	1.2	Attainment target is not reached.
CO4	Identify the functionality of logic gates, parity code and hamming code techniques for error detection and correction of single bit in digital systems.	0.6	2.2	0.9	Attainment target is not reached.
CO5	Construct the combinational logic circuits using appropriate logic gates.	1.3	2.2	1.5	Attainment target is not reached.
CO6	Implement the synchronous and asynchronous counters for memory storing applications.	0.6	2.2	0.9	Attainment target is not reached.

Action taken report:

CO 2: Need to provide more knowledge on transistor characteristics to get on voltage levels.

CO4: Need to provide more knowledge on functionality of logic gates, parity code and hamming code techniques.

CO5 & CO 6: Need to provide more concept and understanding of the logic gates and memory storing applications.

 Course Coordinator	 Mentor	 HOD
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Head of the Department
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