



INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)

Dundigal, Hyderabad -500 043

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

ATTAINMENT OF COURSE OUTCOMES (COS) – ACTION PLAN

Name of the Faculty	Dr. A. Naresh kumar	Department	CSE
Regulations	R16	Batch	2016 -2020
Course Name	Fundamentals of electrical and electronics engineering	Course Code	AEE001
Semester	II	Target Value	70% (2.1 on 3 Scale)

Attainment of COs:

Course Outcomes		Direct Attainment	Indirect Attainment	Overall Attainment	Observation
CO1	Solve complex electrical circuits by applying network reduction techniques for reducing into a simplified circuit.	0.9	2.3	1.2	Target not attained
CO2	Make use of various network theorems and graph theory for simplifying complex electrical networks.	1.6	2.2	1.7	Target not attained
CO3	Define basic nomenclature of single-phase AC circuits for obtaining impedance, admittance of series and parallel circuits.	0.9	2.3	1.2	Target not attained
CO4	Interpret the power factor in single phase circuits with various combination of network elements for computing active and reactive power.	0.6	2.2	0.9	Target not attained
CO5	Apply the PN junction characteristics for the diode applications such as switch and rectifier.	1.6	2.2	1.7	Target not attained
CO6	Extend the biasing techniques for bipolar and uni-polar transistor amplifier circuits considering stability condition for establishing a proper operating point.	0.9	2.2	1.2	Target not attained

Action taken report:

CO1, CO2: Problem solving questions on network reduction techniques and network theorems to be provided during tutorial sessions to enhance student skills for designing simple electrical networks.

CO3, CO4: Need to arrange expert talks on importance of electrical and electronics fundamentals in CSE by enlightening students on circuit theory and transistors.

CO5, CO6: Demonstrations on relevant topics in lab need to be conducted to make students understand applications of diodes transistors.



Course Coordinator



Mentor



Head of the Department **HOD**
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