



# INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)

Dundigal, Hyderabad -500 043

## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

### ATTAINMENT OF COURSE OUTCOMES (COS) – ACTION PLAN

Name of the Faculty	Dr. M. Mohana Roopa	Department	CSE
Regulations	R16	Batch	2017 -2021
Course Name	Computer Organization and Architecture	Course Code:	ACS004
Semester	III	Target Value	70% (2.1 on 3 Scale)

#### Attainment of COs:

Course Outcomes		Direct Attainment	Indirect Attainment	Overall Attainment	Observation
CO1	Illustrate interaction of components in a computer system with functional units and levels of programming languages.	0.9	2.4	1.2	Target not attained
CO2	Demonstrate the implementation of micro-operations with the help of register transfer language and electronic circuits.	0.9	2.4	1.2	Target not attained
CO3	Identify appropriate addressing modes for specifying the location of an operand.	0.6	2.4	1	Target not attained
CO4	Make use of number system for data representation and binary arithmetic in digital computers.	0.6	2.4	1	Target not attained
CO5	Interpret the design of hardwired and micro-programmed control unit for execution of micro programs.	0.9	2.4	1.2	Target not attained
CO6	Summarize the concepts of pipelining and inter process communication for advanced processor design	0.9	2.4	1.2	Target not attained

#### Action taken report:

CO1 & CO2: Extra tutorial classes and assignments need to be organized on assemble instruction set, data transfer methods.

CO3: Extra tutorial classes and assignments need to be organized on addressing modes such that student may understand various modes of retrieve the instructions and data from the memory.

CO4: Practical demonstration on implementation of micro-operations need to be conducted.

CO5 & CO6: Guest lectures need to be organized for hardwired control, multiprocessor & pipelining, such that students can understand the topics well.

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