



# INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)

Dundigal, Hyderabad -500 043

## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

### ATTAINMENT OF COURSE OUTCOMES (COS) – ACTION PLAN

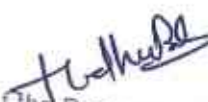
Name of the Faculty	Dr. A. Naresh kumar	Department	CSE
Regulations	R16	Batch	2017-2021
Course Name	Electrical and Electronics Engineering Laboratory	Course Code	AEE101
Semester	II	Target Value	60% (1.8 on 3 Scale)

#### Attainment of COs:

Course Outcomes		Overall Attainment	Observation
CO1	Solve the electrical circuit source resistance, currents, voltage and power by applying various network reduction techniques using hardware and software.	3	Target attained
CO2	Apply various network theorems to reduce complex network into simple equivalent network with DC excitation using hardware and software.	3	Target attained
CO3	Acquire basic knowledge on the working of PN-junction diode, Zener diode to plot their V-I characteristics.	3	Target attained
CO4	Identify transistor configuration and their working to deduce its working as switch and amplifier.	3	Target attained
CO5	Explore the knowledge and skills of employability to succeed in national and international level competitive examinations.	3	Target attained

  
Course Coordinator

  
Mentor

  
Head of the Department  
Computer Science and Engineering  
HOD  
INSTITUTE OF AERONAUTICAL ENGINEERING  
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