INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)
Dundigal, Hyderabad - 500 043

COMPUTER SCIENCE AND INFORMATION TECHNOLOGY

ATTAINMENT OF COURSE OUTCOME- ACTION TAKEN REPORT

Name of the Faculty:	Dr. B Polaiah	Department:	CSIT
Regulation:	UG20	Batch:	2021-2025
Course Name:	Computer Organization and Architecture	Course Code:	ACSC07
Semester:	III	Target Value:	60% (1.8 on 3 scale)

Attainment of COs:

	Course Outcome	Direct Attainment	Indirect Attainment	Overall Attainment	Observations
CO1	Illustrate interaction of components in a computer system with functional units and levels of programming languages.	0.30	2.20	0.7	Target not Attained
CO2	Demonstrate the implementation of micro-operations with the help of register transfer language and electronic circuits.	0.30	2.20	0.7	Target not Attained
CO3	Identify appropriate addressing modes for specifying the location of an operand.	0.90	2.20	1.2	Target not Attained
CO4	Make use of number system for data representation and binary arithmetic in digital computers.	1.60	2.10	1.7	Target not Attained
CO5	Interpret the design of hardwired and micro-programmed control unit for execution of micro programs.	0.60	2.20	0.9	Target not Attained
CO6	Summarize the concepts of pipelining and inter process communication for advanced processor design.	1.30	2.10	1.5	Target not Attained

Action Taken Report: (To be filled by the concerned faculty / course coordinator)

In this Course CO1,CO2,CO3, CO4, CO5 and CO6 requires additional attention and it is improved by

CO 1, CO6 : Making the students to practice problem solving concepts on pipeline, programming for performing computations using addressing modes and instruction set of a microprocessor.
CO 2: Applying knowledge on the concepts of designing multiplexers, and micro program examples.
CO 3: Giving more problems on floating point arithmetic operations and number system conversions.
CO 4: Conducting fore coming sessions for students to understand the concepts of JDBC connectivity and importance of it in using databases in effective way.
CO 5: Making the students to conduct problem solving on memory organization concept, DMA controller and asynchronous data transfer .

Course Coordinator **Mentor** **HOD**

Head of the Department
Computer Science and Information Technology
Institute of Aeronautical Engineering
Dundigal, Hyderabad - 500043