

## INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous) Dundigal, Hyderabad - 500 043

## COMPUTER SCIENCE AND INFORMATION TECHNOLOGY

## ATTAINMENT OF COURSE OUTCOME- ACTION TAKEN REPORT

Name of the Faculty:	Mr. A Karthik	Department:	CSIT	
Regulation:	UG20	Batch:	2022-2026	
Analog and Digital		Course Code:	AECC08	
Semester: III		Target Value:	60% (1.8 on 3 scale)	

## **Attainment of Cos:**

	Course Outcome	Direct Attainment	Indirect Attainment	Overall Attainment	Observations
CO1	Demonstrate the volt-ampere characteristics of semiconductor devices for finding cut-in voltage, resistance and capacitance.	1.6	2.2	1.7	Target not Attained
CO2	Illustrate half wave and full wave rectifier circuits with filter and without filters used to convert the alternating current in to direct current.	1.6	2.3	1.7	Target not Attained
CO3	Analyze the input and output characteristics of transistor configurations and small signal h-parameter models to determine the input - output resistances, current gain and voltage gain	2.30	2.20	2.3	Target Attained
CO4	Identify the functionality of logic gates, parity code and hamming code techniques for error detection and correction of single bit in digital systems.	3	2.30	2.9	Target Attained
CO5	Make use of appropriate logic gates to implement combinational logic circuits.	2.30	2.30	2.3	Target Attained
CO6	Select a required flip flop to realize synchronous and asynchronous counters for memory storing applications.	1.60	2.30	1.7	Target not Attained

Action Taken Report: (To be filled by the concerned faculty / course coordinator)

In this Course CO1,CO2 and CO6 requires additional attention and it is improved by

- CO 1: Making the Students to acquire basic knowledge about semiconductor devices.
- CO 2: Practicing more number of problems related to rectifiers.
- CO 6: Conducting fore coming sessions for students to understand the concepts of JDBC connectivity and importance of it in using databases in effective way.

Course Coordinator

Mentor

Head of the Department
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