

INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous) Dundigal, Hyderabad - 500 043

ELECTRONICS AND COMMUNICATION ENGINEERING

ATTAINMENT OF COURSE OUTCOME- ACTION TAKEN REPORT

Name of the Faculty:	B.Naresh	Department:	ECE	-
Regulation:	IARE-R16	Batch:	2016-2020	
Course Name:	Electronic Devices and Circuits	Course Code:	AEC001	
Semester:	III	Target Value:	60% (1.8)	

Attainment of Cos:

Course Outcome		Direct Attainment	Indirect Attainment	Overall Attainment	Observations	
CO1	Illustrate the characteristics of semiconductor devices for determining the device parameters such as resistances, current gain and voltage gain.	2.00	2.60	2.1	Attained	
CO2	Apply the pn junction characteristics for the diode applications such as switch and rectifiers.	0.90	2.70	1.3	Not attained	
CO3	Examine DC and AC load line analysis of BJT and FET amplifiers for optimal operating level regardless of input, load placed on the device.	0.60	2.80	1	Not attained	
CO4	Extend the biasing techniques for bipolar and uni-polar transistor for establishing a proper operating point.	0.90	2.60	1.2	Not attained	
CO5	Utilize low frequency model for estimation of the characteristic parameters of BJT, FET amplifier circuits.	0.30	2.80	0.8	Not attained	
CO6	Demonstrate the working principle of special purpose semiconductor diodes and transistors for triggering and voltage regulation applications.	0.90	2.70	1.3	Not attained	

Action Taken Report: (To be filled by the concerned faculty / course coordinator)
In this Course, the CO1, CO2, CO3, CO5 and CO6 requires additional attention and it is improved by

- 1. Conducting Guest lectures on break down mechanisms in semiconductor diodes.
- 2. Additional inputs will be provided on half wave and full wave rectifiers for improving students performance.
- Additional inputs will be provided on Bipolar Junction Transistors, Field Effect Transistors and MOSFET construction and operation.
- 4. Additional inputs will be provided on biasing and bias compensation techniques for improving students performance.
- 5. Giving assignments and conducting tutorials on analysis determination of h-parameters from transistor characteristics

Course Coordinator

Mentor

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