



INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)
Dundigal, Hyderabad - 500 043

ELECTRONICS AND COMMUNICATION ENGINEERING

ATTAINMENT OF COURSE OUTCOME- ACTION TAKEN REPORT

Name of the Faculty:	Mr. S Lakshmanachari	Department:	ECE
Regulation:	IARE-R16	Batch:	2016-2020
Course Name:	Pulse and Digital Circuits	Course Code:	AEC006
Semester:	IV	Target Value:	60% (1.8)


Attainment of Cos:

Course Outcome	Direct Attainment	Indirect Attainment	Overall Attainment	Observations
CO1 Analyze the response of linear and non-linear wave shaping circuits for impulse and pulse inputs with different time constants.	1.60	2.20	1.7	Not attained
CO2 Build bistable, monostable and astable multivibrator circuits using transistors for real time applications.	0.60	2.20	0.9	Not attained
CO3 Apply the operating principles of diodes and transistors for the designing of sampling gates.	0.90	2.20	1.2	Not attained
CO4 Illustrate different methods to generate time base waveforms using Bootstrap and Miller circuits.	0.90	2.10	1.1	Not attained
CO5 Understand the synchronization and frequency division concepts using relaxation devices and sweep circuits.	0.90	2.10	1.1	Not attained
CO6 Summarize the characteristics of digital logic families for designing of digital logic circuits.	1.60	2.20	1.7	Not attained


Action Taken Report: (To be filled by the concerned faculty / course coordinator)

In this Course, the entire COs requires additional attention and it is improved by

1. Additional inputs will be provided on linear and non-linear wave shaping circuits.
2. Conducting Guest lectures on triggering of multivibrators like bi-stable and mono stable.
3. Conducting Guest lectures on time base generators and sampling gates.
4. Giving assignments and conducting tutorials on synchronization of a sweep circuit with symmetrical signals.
5. Additional inputs will be provided on Schmitt Trigger Circuit to determine the pulses correctly.
6. Additional inputs will be provided on multivibrator circuits using transistors for real time applications and digital logic families


Course Coordinator


Mentor


HOD
Head of the Department
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