



INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)
Dundigal, Hyderabad - 500 043

ELECTRONICS AND COMMUNICATION ENGINEERING

ATTAINMENT OF COURSE OUTCOME- ACTION TAKEN REPORT

Name of the Faculty:	Mr.V R Seshagiri Rao	Department:	ECE
Regulation:	IARE-R16	Batch:	2016-2020
Course Name:	VLSI design	Course Code:	AEC017
Semester:	VII	Target Value:	60% (1.8)

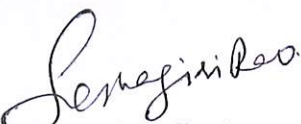
Attainment of Cos:

Course Outcome		Direct Attainment	Indirect Attainment	Overall Attainment	Observations
CO1	Outline the effects of scaling on MOS circuits for area, speed, power and fabrication flow with constant voltage or field or combined modelling	1.70	2.30	1.8	Attained
CO2	Describe inverters, complex gates and dynamic CMOS circuits for power consumption and speed of operation.	2.00	2.30	2.1	Attained
CO3	Build the stick diagrams, layouts of MOS circuits using lambda, absolute and Euler physical design rules.	2.70	2.30	2.6	Attained
CO4	Distinguish the structure, implementation approaches on full custom and semicustom design for speed, cost, reconfiguration and reliability parameters.	1.60	2.40	1.8	Attained
CO5	Explain data path subsystems containing arithmetic logic units, parity generators, comparators and memories using stick diagrams and layouts.	2.30	2.40	2.3	Attained
CO6	Construct simulation, synthesis and design verification of logic circuits using the key elements of VLSI design flow.	0.90	0.00	0.7	Not Attained

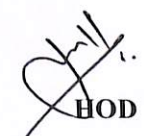
Action Taken Report: (To be filled by the concerned faculty / course coordinator)

In this Course, the CO6 requires additional attention and it is improved by

1. Conducting Guest lectures on Embedded Systems programming in C with Keil Integrated Development Environment.
2. Additional inputs will be provided on simulation, synthesis and design verification of logic circuits for better understanding.
3. Giving assignments and conducting tutorials on implementation approaches on full custom and semicustom design for speed, cost, and reconfiguration for saving memory for more practice.


Course Coordinator


Mentor


HOD
Head of the Department
Electronics and Communication Engineering
INSTITUTE OF AERONAUTICAL ENGINEERING
Dundigal, Hyderabad - 500 043