



Dundigal, Hyderabad - 500043, Telangana

## **ELECTRONICS AND COMMUNICATION ENGINEERING**

## ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT

| Name of the faculty: | Mr. V R SESHAGIRI RAO                      | Department:   | Electronics and Communication Engineering |  |
|----------------------|--------------------------------------------|---------------|-------------------------------------------|--|
| Regulation:          | IARE - R20                                 | Batch:        | 2021-2025                                 |  |
| Course Name:         | Electronic Devices and Circuits Laboratory | Course Code:  | AECC05                                    |  |
| Semester:            | III                                        | Target Value: | 60% (1.8)                                 |  |

## **Attainment of COs:**

| Course Outcome |                                                                                                                                                       | Direct<br>Attainment | Indirect<br>Attainment | Overall<br>Attainment | Observation |
|----------------|-------------------------------------------------------------------------------------------------------------------------------------------------------|----------------------|------------------------|-----------------------|-------------|
| CO1            | Demonstrate the electronic instruments for measuring voltage, current and phase parameters.                                                           | 2.30                 | 0.00                   | 2.3                   | Attained    |
| CO2            | Experiment and determine the parameters of rectifiers and voltage regulators using the diode characteristics.                                         | 2.30                 | 0.00                   | 2.3                   | Attained    |
| CO3            | Examine the input and output characteristics of transistor (BJT and FET) configurations for determining the input - output resistances                | 2.30                 | 0.00                   | 2.3                   | Attained    |
| CO4            | Characterize BJT and FET amplifiers for estimating the voltage gain and Current gain.                                                                 | 2.30                 | 0.00                   | 2.3                   | Attained    |
| CO5            | Assess the outputs achieved by making judgments about information and validity of ideas for confirming the quality of work based on a set of criteria | 2.30                 | 0.00                   | 2.3                   | Attained    |
| CO6            | Design a hardware prototype to test and analyze the product designed for an application                                                               | 2.30                 | 0.00                   | 2.3                   | Attained    |

Action Taken Report: (To be filled by the concerned faculty / course coordinator)

Course Coordinator

Mentor

Head of the Department

Dr. P. MUNASWAMY M.Tech, Ph.D, MISTE
Professor & Head
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