



(Autonomous)

Dundigal, Hyderabad - 500043, Telangana

ELECTRONICS AND COMMUNICATION ENGINEERING

ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT

Name of the faculty: Mr. B NARESH		Department:	Electronics and Communication Engineering	
Regulation:	IARE - R20	Batch:	2022-2026	
Course Name:	Electronic Devices and Circuits	Course Code:	AECC01	
Semester:	III	Target Value:	60% (1.8)	

Attainment of COs:

Course Outcome		Direct Attainment	Indirect Attainment	Overall Attainment	Observation
CO1	Illustrate the characteristics of semiconductor devices for determining the device parameters such as resistances, current gain and voltage gain.	2.40	2.20	2.4	Attained
CO2	Apply the pn junction characteristics for the diode applications such as switch, rectifiers, Clippers and Clampers.	2.40	2.20	2.4	Attained
CO3	Examine DC and AC load line analysis of BJT and FET amplifiers for optimal operating level regardless of input, load placed on the device.	3.00	. 2.20	2.8	Attained
CO4	Extend the biasing techniques for bipolar and uni-polar transistor amplifier circuits considering stability condition for establishing a proper operating point	Z .10	2.20	2.1	Attained
CO5	Utilize low frequency model for estimation of the characteristic parameters of BJT, FET amplifier circuits.	2.70	2.20	2.6	Attained
C06	Demonstrate the working principle of special purpose semiconductor diodes and transistors for triggering and voltage regulation applications.	2.10	2.20	2.1	Attained

Action Taken Report: (To be filled by the concerned faculty / course coordinator)

Course Coordinator

Mentor

Head of the Department

Head of the Department
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