



Dundigal, Hyderabad - 500043, Telangana

ELECTRONICS AND COMMUNICATION ENGINEERING

ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT

Name of the faculty:	Mr. A SRIKANTH	Department:	Electronics and Communication Engineering		
Regulation:	IARE - R20	Batch:	2022-2026		
Course Name:	Electrical Circuits	Course Code:	AEEC02		
Semester:	II	Target Value:	60% (1.8)		

Attainment of COs:

Course Outcome		Direct Attainment	Indirect Attainment	Overall Attainment	Observation
CO1	Identify the basic concepts of electrical quantities such as current, voltage, power, energy of simple DC circuits used in electrical and electronic devices.	0.90	2.30	1.2	Not Attained
CO2	Define basic terminology of single-phase AC circuits for obtaining mean value, RMS value, form facto, peak facto, impedance, admittance, apparent, real power, reactive power and power factor of electrical circuits.	2.00	2.30	2.1	Attained
CO3	Apply the different laws, series parallel combination of RLC circuits and indirect quantities associated with electrical circuit for determine voltage and currents in resistive circuits containing voltage and current sources.	0.90	2.30	1.2	Not Attained
CO4	Apply the several theorems for simplify complex network into equivalent network and verify the current, voltage and power in linear bilateral network with the help of DC and AC excitation.	0.30	2.30	0.7	Not Attained
CO5	Describe the basic fundamental of Electromagnetism, Faraday's laws of Electromagnetic induction, Lenz's law, types of induced emf, self and mutual inductance for notice the total magnetomotive force and ampere turns values.	2.30	2.30	2.3	Attained
CO6	Understand the two port parameters, network topology and dual network for digital and graphical representation of complex circuits to be measure easily, without solving for all the internal voltages and currents in the different networks.	1.60	2.30	1.7	Not Attained

Action Taken Report: (To be filled by the concerned faculty / course coordinator)

CO1: Make practice of problems on current, voltage, power, energy of simple DC circuits used in electrical and electronic devices.

CO3: Guest lectures will be conduct on different laws, series parallel combination of RLC circuits and indirect quantities associated with electrical circuit

CO4: Assignments will be given on several theorems for simplify complex network into equivalent network and verify the current, voltage.

CO6: Guest lectures will be conduct on two port parameters, network topology and dual network for digital and graphical representation of complex circuits

Course Coordinator

Mentor

Head of the Department

Head of the Department
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