



INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)

Dundigal, Hyderabad - 500 043

ELECTRICAL AND ELECTRONICS ENGINEERING

ATTAINMENT OF COURSE OUTCOME – ACTION TAKEN REPORT

Name of the faculty:	S Rambabu	Department:	EEE
Regulation:	IARE - R16	Batch:	2016 - 2020
Course Name:	DIGITAL AND PULSE CIRCUITS	Course Code:	AEC019
Semester:	IV	Target Value:	60% (1.8)

Attainment of COs:

Course Outcome	Direct attainment	Indirect attainment	Overall attainment	Observation
CO1 Understand the different forms of number representations and binary codes in digital logic circuits.	3	2.4	2.9	Attained
CO2 Make use of Boolean postulates, theorems and k-map for obtaining minimized Boolean expressions.	2.3	2.4	2.3	Attained
CO3 Implement the combinational logic circuits using the logic gates.	0.9	2.4	1.2	Not Attained
CO4 Utilize the functionality and characteristics of flip-flops and latches for designing sequential circuits	0.9	2.3	1.2	Not Attained
CO5 Obtain the expression to find frequency of oscillations for RC and LC type oscillator circuits	2.3	2.4	2.3	Attained
CO6 Illustrate Bipolar Junction Transistor (BJT) amplifier circuits and their frequency responses at low, mid and high frequencies for determining amplifier characteristics.	0.9	2.3	1.2	Not Attained

Action taken report:

CO 3: Need to focus on combinational logic circuits
CO 4: More focus on the functionality and characteristics of flip-flops and latches
CO 6: Provide more focus on Bipolar Junction Transistor (BJT) amplifier circuits

Course Coordinator

Mentor

Head of the Department
Electrical and Electronics Engineering
HOD
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