



INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)

Dundigal, Hyderabad - 500 043

ELECTRICAL AND ELECTRONICS ENGINEERING

ATTAINMENT OF COURSE OUTCOME – ACTION TAKEN REPORT

Name of the faculty:	Chiranjeevi Thokala	Department:	EEE
Regulation:	IARE - R16	Batch:	2016 - 2020
Course Name:	Microcontrollers and Digital Signal Processing	Course Code:	AEC022
Semester:	VI	Target Value:	60% (1.8)

Attainment of COs:

Course Outcome		Direct attainment	Indirect attainment	Overall attainment	Observation
CO1	Illustrate the basic concept of digital fundamentals to architecture of microprocessors and microcontroller	0.3	2.3	0.7	Not Attained
CO2	Recall the addressing modes and instruction set of 8085 microprocessor for generating assembly programs	0.3	2.3	0.7	Not Attained
CO3	Describe the architecture and instruction set of 8051 microcontroller for Designing and implement 8051 microcontroller based systems.	0.3	2.3	0.7	Not Attained
CO4	Construct the decimation-in-time and decimation-in-frequency fast fourier transforms for reducing computational complexity of DFT.	0.3	2.4	0.7	Not Attained
CO5	Implement FIR and IIR filters using digital filter transformation techniques	0.6	2.3	0.9	Not Attained
CO6	Make use of architecture and programming of 8051 microcontroller for implementing digital signal processing applications in DSP processor.	0.3	2.3	0.7	Not Attained

Action taken report:

CO 1: Need to use digital resources and ICT tools
CO 2: Provide more assignments on addressing modes and instruction set of 8085 microprocessor
CO 3: Need to focus on architecture and instruction set of 8051 microcontroller
CO 4: More focus on decimation-in-time and decimation-in-frequency
CO 5: Provide more focus on FIR and IIR filters
CO 6: Need to focus on architecture and programming of 8051 microcontroller

Course Coordinator

Mentor

Head of the Department
Electrical and Electronics Engineering
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