



INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)

Dundigal, Hyderabad - 500 043

ELECTRICAL AND ELECTRONICS ENGINEERING

ATTAINMENT OF COURSE OUTCOME – ACTION TAKEN REPORT

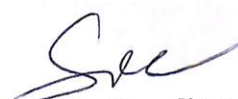
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|----------------------|--|---------------|-------------|
| Name of the faculty: | Ms. J Sravana | Department: | EEE |
| Regulation: | IARE - R16 | Batch: | 2016 - 2020 |
| Course Name: | Integrated Circuits Applications Laboratory | Course Code: | AEC106 |
| Semester: | V | Target Value: | 60% (1.8) |

Attainment of COs:

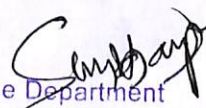
| Course Outcome | | Overall attainment | Observation |
|----------------|---|--------------------|-------------|
| CO1 | Design linear Integrated circuits to perform mathematical operations and voltage gain calculations using IC741. | 2.7 | Attained |
| CO2 | Plot the frequency response of second order active filters using IC 741. | 2.7 | Attained |
| CO3 | Determine the frequency of oscillations of multi-vibrators using IC741 and IC555 timer. | 2.7 | Attained |
| CO4 | Obtain the capture range and lock-in range of phase locked loop circuit using IC565. | 2.7 | Attained |
| CO5 | Construct the low and high voltage regulators to find the percentage of regulation using IC723. | 2.7 | Attained |
| CO6 | Implement combinational and sequential circuits using digital ICs to verify their functionality. | 2.7 | Attained |

Action taken report:

All the course outcomes are attained the target value.


Course Coordinator


Mentor


Head of the Department
Electrical and Electronic Engineering
HOD
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