



INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)

Dundigal, Hyderabad - 500 043

ELECTRICAL AND ELECTRONICS ENGINEERING

ATTAINMENT OF COURSE OUTCOME – ACTION TAKEN REPORT

Name of the faculty:	B. Naresh	Department:	EEE
Regulation:	IARE - R16	Batch:	2016 - 2020
Course Name:	Electronic Circuits Laboratory	Course Code:	AEC113
Semester:	III	Target Value:	60% (1.8)

Attainment of COs:

Course Outcome		Overall attainment	Observation
CO1	Demonstrate electronic measuring instruments for measuring voltage, current, frequency and phase of the various signals.	2	Attainment target reached
CO2	Apply the pn junction diode and Zener diode characteristics for the diode applications such as rectifiers and voltage regulator	2	Attainment target reached
CO3	Analyze the input and output characteristics of transistor(BJT and FET) configurations for determining the input - output resistances.	2	Attainment target reached
CO4	Analyze BJT and FET amplifiers for estimating the voltage gain and Current gain	2	Attainment target reached
CO5	Calculate the intrinsic stand-off ratio of the uni junction transistor using volt – ampere characteristics	2	Attainment target reached
CO6	Determine holding, latching current and break over voltage of silicon controlled rectifier using volt – ampere characteristics.	2	Attainment target reached

Action taken report:

All the course outcomes are attained the target value

Course Coordinator

Mentor

Head of the Department
Electrical and Electronics Engineering
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HOD