



INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)

Dundigal, Hyderabad - 500 043

ELECTRICAL AND ELECTRONICS ENGINEERING

ATTAINMENT OF COURSE OUTCOME – ACTION TAKEN REPORT

Name of the faculty:	Ms. J Sravana	Department:	EEE
Regulation:	IARE - R16	Batch:	2017 - 2021
Course Name:	Microcontrollers and Digital Signal Processing	Course Code:	AEC022
Semester:	VI	Target Value:	60% (1.8)

Attainment of COs:

Course Outcome		Direct attainment	Indirect attainment	Overall attainment	Observation
CO1	Illustrate the basic concept of digital fundamentals to architecture of microprocessors and microcontroller	3	2.6	2.9	Attained
CO2	Recall the addressing modes and instruction set of 8085 microprocessor for generating assembly programs	3	2.5	2.9	Attained
CO3	Describe the architecture and instruction set of 8051 microcontroller for Designing and implement 8051 microcontroller based systems.	3	2.5	2.9	Attained
CO4	Construct the decimation-in-time and decimation-in-frequency fast fourier transforms for reducing computational complexity of DFT.	3	2.5	2.9	Attained
CO5	Implement FIR and IIR filters using digital filter transformation techniques	3	2.6	2.9	Attained
CO6	Make use of architecture and programing of 8051 microcontroller for implementing digital signal processing applications in DSP processor.	3	2.6	1.2	Not Attained

Action taken report:

CO 6: Need to focus on architecture and programing of 8051 microcontroller

Course Coordinator

Mentor

Head of the Department
Electrical and Electronics **HOD** Engineering
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