

(Autonomous)

Dundigal, Hyderabad - 500043, Telangana

ELECTRICAL AND ELECTRONICS ENGINEERING

ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT

Name of the faculty:

Mr. N NAGARAJU

Department:

Electrical and Electronics Engineering

Regulation:

IARE - R18

Batch:

2018-2022

Course Name:

ANALOG AND DIGITAL ELECTRONICS LABORATORY

Course Code:

AECB04

Semester:

111

Target Value:

60% (1.8)

Attainment of COs:

	Course Outcome	Direct Attainment	Indirect Attainment	Overall Attainment	Observation
CO1	Make use of volt-ampere characteristics of pn junction diode, Zener diode for finding the cut-in voltage, static dynamic resistances and voltage regulation.	3.00	0.00	3	Attained
CO2	Apply the pn junction characteristics for the diode applications such as half wave rectifier and full wave rectifier.	3.00	0.00	3	Attained
CO3	Analyze the input and output characteristics of transistor configurations for determining the input - output resistances and voltage gain.	3.00	0.00	3	Attained
CO4	Identify the functionality of the Boolean expressions using basic gates such as not, and, or, nand, nor, xor and xnor gates.	3.00	0.00	3	Attained
CO5	Build combinational circuits such as adder, subtractor, multiplexers and comparators realization using low level elementary blocks.	3.00	0.00	3	Attained
CO6	Construct shift registers using the functionality of the flip flops.	3.00	0.00	3	Attained

Action Taken Report: (To be filled by the concerned faculty / course coordinator)

Course Coordinator

Mentor

Head of the Department