

Dundigal, Hyderabad - 500043, Telangana

## ELECTRICAL AND ELECTRONICS ENGINEERING

## ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT

Name of the faculty:	Dr. P SRIDHAR	Department:	Electrical and Electronics Engineering	
Regulation:	IARE - R18	Batch:	2018-2022	
Course Name:	CONTROL SYSTEMS	Course Code:	AEEB16	
Semester:	ıv	Target Value:	60% (1.8)	

## Attainment of COs:

	Course Outcome	Direct attaiment	Indirect attaiment	Overall attaiment	Observation
)1	Relate the different physical and mechanical systems into equivalent electrical analogies using the mathematical form of complex physical systems.	1.60	2.90	1.9	Attained
)2	Utilize various reduction techniques for developing the transfer function and steady state error with the standard input signals.	2.00	2.30	2.1	Attained
03	Make use of the time domain analysis to predict transient response specifications for analysing system's stability	2.30	2.50	2.3	Attained
D4	Infer the stability of a first and second order systems using frequency domain specifications	1.70	3.00	2 .	<sup>*</sup> Attained
<b>D</b> 5	Classify the types of compensators in time domain and frequency domains specifications for increasing the steady state accuracy of the system.	0.30	2.90	0.8	Not Attained
<b>D</b> 6	Interpret linear system equations in state-variable form for the analysis of system's dynamic behavior.	0.30	1.80	0.6	Not Attained

Action taken report:

CO5:

More problems should be practiced

CO6:

Tutorial classes has been conducted

Course Coordinator

Head of the Department

Head of the Department
Electrical and Electronics Engineering
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