

(Autonomous) Dundigal, Hyderabad - 500043, Telangana

## ELECTRICAL AND ELECTRONICS ENGINEERING

## ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT

Name of the faculty:

Dr. P SRIDHAR

Department:

**Electrical and Electronics Engineering** 

Regulation:

IARE - R18

Batch:

2018-2022

Course Name:

CONTROL SYSTEMS LABORATORY

Course Code:

Semester:

IV

AEEB18

Target Value:

60% (1.8)

Attainm	ent of COs:				
	Course Outcome	Direct Attainment	Indirect Attainment	Overall Attainment	Observation
CO1	Make use of the knowledge of digital simulation tool for system analysis with different standard inputs	1.60	0.00	1.6	Not Attained
CO2	Model the dynamic systems in transfer function for evaluating the performance characteristics of motors.	1.60	0.00	1.6	Not Attained
CO3	Analyse and select various electronics devices and Compensation techniques for improving system performance with digital simulation	1.60	0.00	1.6	Not Attained
CO4	Analyse the system's stability in time and frequency domain by computing gain and phase margin.	1.60	0.00	1.6	Not Attained
CO5	Land and the same basic PLC circuits for automated	1.60	0.00	1.6	Not Attained

## Action Taken Report: (To be filled by the concerned faculty / course coordinator)

CO1: More problems should be practiced

CO2: Extra lab have been taken

CO3: Students are encouraged videos

CO4: Model based learning are planned

CO5: Students are encouraged to ELRV videos