

(Autonomous)

Dundigal, Hyderabad - 500043, Telangana

## ELECTRICAL AND ELECTRONICS ENGINEERING

## ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT

Name of the faculty:

Ms. BK AISHWARYA

Department:

**Electrical and Electronics Engineering** 

Regulation:

IARE - R18

Batch:

2018-2022

Course Name:

Engineering Graphics and Design Laboratory

Course Code:

AMEB02

Target Value:

60% (1.8)

Semester:

## **Attainment of COs:**

	Course Outcome	Direct Attainment	Indirect Attainment	Overall Attainment	Observation
CO1	Illustrate bureau of Indian standards conventions of engineering drawing with basic concepts, ideas and methodology for different geometries and their execution	1.30	0.00	1.3	Not Attained
CO2	Apply the commands used in AutoCAD for development of multi-aspect sketches, additional and sectional view.	1.30	0.00	1.3	Not Attained
CO3	Construct parabolic, Hyperbolic and elliptical curves for profiles likes buildings and bridges. Build Cycloidal and involutes profiles for developing new products like gears and other engineering applications.	1.30	0.00	1.3	Not Attained
CO4	Explain various types of scales for engineering applications like maps, buildings, bridges	1.30	0.00	1.3	Not Attained
CO5	Explain the concept of projection of solids inclined to both the planes for interpretation of different views and orthographic projection concepts in solid modeling	1.30	0.00	1.3	Not Attained
CO6	Recall the orthographic projection concepts in solid modeling for use in conversation to isometric and Vice-versa	1.30	0.00	1.3	Not Attained

## Action Taken Report: (To be filled by the concerned faculty / course coordinator)

CO1: Extra classes should be taken

CO2: More problems should be practiced

CO3: Students are encouraged to ELRV videos

CO4: Model based learning are planned

CO5: Extra lab have been taken

CO6: More problems should be practiced