

**ELECTRICAL AND ELECTRONICS ENGINEERING****ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT**

Name of the faculty:	<b>Ms.T SARITHA KUMARI</b>	Department:	<b>Electrical and Electronics Engineering</b>
Regulation:	<b>IARE - R18</b>	Batch:	<b>2019-2023</b>
Course Name:	<b>ANALOG AND DIGITAL ELECTRONICS LABORATORY</b>	Course Code:	<b>AECB04</b>
Semester:	<b>III</b>	Target Value:	<b>70% (2.1)</b>

**Attainment of COs:**

	<b>Course Outcome</b>	<b>Direct Attainment</b>	<b>Indirect Attainment</b>	<b>Overall Attainment</b>	<b>Observation</b>
CO1	Make use of volt-ampere characteristics of pn junction diode, Zener diode for finding the cut-in voltage, static dynamic resistances and voltage regulation.	0.90	0.00	0.9	Not Attained
CO2	Apply the pn junction characteristics for the diode applications such as half wave rectifier and full wave rectifier.	0.90	0.00	0.9	Not Attained
CO3	Analyze the input and output characteristics of transistor configurations for determining the input - output resistances and voltage gain.	0.90	0.00	0.9	Not Attained
CO4	Identify the functionality of the Boolean expressions using basic gates such as not, and, or, nand, nor, xor and xnor gates.	0.90	0.00	0.9	Not Attained
CO5	Build combinational circuits such as adder, subtractor, multiplexers and comparators realization using low level elementary blocks.	0.90	0.00	0.9	Not Attained
CO6	Construct shift registers using the functionality of the flip flops.	0.90	0.00	0.9	Not Attained

**Action Taken Report: (To be filled by the concerned faculty / course coordinator)**

CO1: Provide assignments on volt-ampere characteristics of pn junction diode, Zener diode for finding the cut-in voltage, static dynamic resistances and voltage regulation

CO2: Extra classes on pn junction characteristics for the diode applications such as half wave rectifier and full wave rectifier.

CO3: Extra lab classes on input and output characteristics of transistor configurations for determining the input - output resistances and voltage gain.

CO4: Use digital resources for better understanding of functionality of the Boolean expressions using basic gates such as not, and, or, nand, nor, xor and xnor gates.

CO5: Conduct tutorial classes on building combinational circuits such as adder, subtractor, multiplexers and comparators realization using low level elementary blocks.

CO6: Use digital resources for shift registers using the functionality of the flip flops.

  
**Course Coordinator**
  
**Mentor**
  
**Head of the Department**