

**ELECTRICAL AND ELECTRONICS ENGINEERING**
ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT

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|----------------------|---------------------------------------------|---------------|-----------------------------------------------|
| Name of the faculty: | Ms. B LAKSHMI PRASANNA | Department: | Electrical and Electronics Engineering |
| Regulation: | IARE - R18 | Batch: | 2019-2023 |
| Course Name: | Microprocessors and Microcontrollers | Course Code: | AECB24 |
| Semester: | V | Target Value: | 60% (1.8) |

Attainment of COs:

| Course Outcome | | Direct Attainment | Indirect Attainment | Overall Attainment | Observation |
|----------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------|-------------------|---------------------|--------------------|--------------|
| CO1 | Outline the functional components of microprocessors and microcontrollers for understanding the operation of architectures. | 2.30 | 2.40 | 2.3 | Attained |
| CO2 | Make use of addressing modes and instruction set of target microprocessors and microcontrollers for writing an assembly language programs to perform a task. | 1.60 | 2.40 | 1.8 | Attained |
| CO3 | Demonstrate the internal architecture and modes of operation of peripheral devices for interfacing memory and I/O devices. | 0.90 | 2.40 | 1.2 | Not Attained |
| CO4 | Illustrate the interrupt handling mechanism in microprocessors and microcontrollers using interrupt controller. | 1.60 | 2.40 | 1.8 | Attained |
| CO5 | Choose an appropriate data transfer scheme and hardware for data transfer between the devices. | 1.60 | 2.40 | 1.8 | Attained |
| CO6 | Develop microprocessor and microcontroller based applications using necessary input and output devices. | 0.90 | 2.40 | 1.2 | Not Attained |

Action Taken Report: (To be filled by the concerned faculty / course coordinator)

CO3: Use digital resources for better understanding of internal architecture and modes of operation of peripheral devices for interfacing memory and I/O devices.

CO6: Conduct tutorial classes for microprocessor and microcontroller based applications using necessary input and output devices.


Course Coordinator


Mentor


Head of the Department