



(Autonomous) Dundigal, Hyderabad - 500043, Telangana

## ELECTRICAL AND ELECTRONICS ENGINEERING

## ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT

Name of the faculty: Ms. CSL VIJAYA DURGA		Department:	Electrical and Electronics Engineering	
Regulation:	IARE - R20	Batch:	2020-2024	
Course Name:	Programming for Problem Solving using C	Course Code:	ACSC04	
Semester: II		Target Value: 60% (1.8)		

## Attainment of COs:

Course Outcome		Direct Attainment	Indirect Attainment	Overall Attainment	Observation
CO1	Define the algorithms and draw flowcharts for solving Mathematical and Engineering problems.	0.90	2.20	1.2	Not Attained
CO2	Construct programs for decision structures and loops.	1.60	2.20	1.7	Not Attained
CO3	Interpret various types of functions, arrays, and strings for complex problem solving.	1.00	2.10	1.2	Not Attained
CO4	Illustrate he dynamic memory allocation, structures, unions and enumerations to solve problems.	1.30	2.20	1.5	Not Attained
CO5	Interpret file input and output functions to do integrated programming.	1.40	2.10	1.5	Not Attained
CO6	Utilize the algorithms in C language to real-life computational problems.	0.00	2.10	0.4	Not Attained

## Action Taken Report: (To be filled by the concerned faculty / course coordinator)

CO1: Solving Mathematical and Engineering problems.

CO2: Students are encouraged to do mooc courses

CO3: Students are encouraged to ELRV videos

CO4: Extra classes should be taken

CO5: More practice on Interpret file input and output functions to do integrated programming.

CO6: Explain the algorithms in C language to real-life computational problems.

Course Coordinator

Mentor

Head of the Department