

(Autonomous)

Dundigal, Hyderabad - 500043, Telangana

ELECTRICAL AND ELECTRONICS ENGINEERING

ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT

Name of the faculty:	Ms. CSL VIJAYA DURGA	Department:	Electrical and Electronics Engineering	
Regulation:	IARE - R20	Batch: 2020-2024		
Course Name:	Programming for Problem Solving using C Laboratory	Course Code:	ACSC05	
Semester:	11	Target Value:	60% (1.8)	

Attainment of COs:

Course Outcome		Direct Attainment	Indirect Attainment	Overall Attainment	Observation
CO1	Demonstrate the problem-solving steps in terms of algorithms, pseudocode and flowcharts for Mathematical and Engineering problems.	0.90	0.00	0.9	Not Attained
CO2	Make use of the concept of operators, precedence of operators, conditional statements and looping statements to solve real time applications	0.90	0.00	0.9	Not Attained
CO3	Demonstrate the concept of pointers, arrays and perform pointer arithmetic, and use the pre-processor.	0.90	0.00	0.9	Not Attained
CO4	Construct programs involving derived data types like structures and union to solve complex programs.	0.90	0.00	0.9	Not Attained
CO5	Make use of various types of functions, parameters, and return values for complex problem solving.	0.90	0.00	0.9	Not Attained
CO6	Implement the programs with concept of file handling functions and pointer with real time applications of C.	0.90	0.00	0.9	Not Attained

Action Taken Report: (To be filled by the concerned faculty / course coordinator)

CO1: solving steps in terms of algorithms, pseudocode and flowcharts for Mathematical and Engineering problems.

CO2: concept of operators, precedence of operators, conditional statements and looping statements to solve real time applications

CO3: concept of pointers, arrays and perform pointer arithmetic, and use the pre-processor.

CO4: Extra lab have been taken

CO5: Students are encouraged to ELRV videos

CO6: More problems should be practiced

Dryff Course Coordinator Mentor

Head of the Departme