



ELECTRICAL AND ELECTRONICS ENGINEERING  
ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT

Name of the faculty:	Ms. V BINDU SREE	Department:	Electrical and Electronics Engineering
Regulation:	IARE - R20	Batch:	2020-2024
Course Name:	Digital Electronics	Course Code:	AECC16
Semester:	IV	Target Value:	60% (1.8)

Attainment of COs:

Course Outcome		Direct Attainment	Indirect Attainment	Overall Attainment	Observation
CO1	Understand the different forms of number representations and binary codes in digital logic circuits	1.70	2.20	1.8	Attained
CO2	Make use of Boolean postulates, theorems and k-map for obtaining minimized Boolean expressions	0.30	2.10	0.7	Not Attained
CO3	Utilize the functionality and characteristics of flip-flops and latches for designing sequential circuits	2.30	2.20	2.3	Attained
CO4	Construct the synchronous and asynchronous modules using flip-flops used for memory storing applications	1.30	2.20	1.5	Not Attained
CO5	Choose an appropriate A/D and D/A converters for signal processing applications	3.00	2.20	2.8	Attained
CO6	Extend the knowledge of memories and programmable logic devices for understanding the architectural blocks of FPGA	1.70	2.20	1.8	Attained

Action Taken Report: (To be filled by the concerned faculty / course coordinator)

CO2: More problems should be practiced

CO4: Model based learning are planned

  
Course Coordinator

  
Mentor

  
Head of the Department