

(Autonomous)

Dundigal, Hyderabad - 500043, Telangana

## **ELECTRICAL AND ELECTRONICS ENGINEERING**

## ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT

Name of the faculty:

Dr. G SESHADRI

Department:

**Electrical and Electronics Engineering** 

Regulation:

IARE - UG20

Batch:

2020-2024

Course Name:

**Power System Stability** 

Course Code:

AEEC36

Semester:

VII

Target Value:

60% (1.8)

## Attainment of COs:

	Course Outcome	Direct Attainment	Indirect Attainment	Overall Attainment	Observation
CO1	Illustrate the significance of power system stability and approach for analysis of multi machine system. using conventional optimization techniques and general transmission line loss formula	3.00	2.10	2.8	Attained
CO2	Develop the equivalent circuits for mathematical analysis of the synchronous machines.	1.30	2.10	1.5	Not Attained
CO3	Explain the methods to enhance the small signal stability of the power system	2.30	2.10	2.3	Attained
CO4	Choose different solving Techniques for improving transient stability	3.00	2.10	2.8	Attained
CO5	Explain various methods to enhance the voltage stability	2.40	2.10	2.3	Attained

Action Taken Report: (To be filled by the concerned faculty / course coordinator)

CO2: Assignment was given

Course Good Instar

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Head of the Department