



INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)

Dundigal, Hyderabad - 500043, Telangana

ELECTRICAL AND ELECTRONICS ENGINEERING

ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT

Name of the faculty: **Ms. C V P SUPRADEEPTHI** Department: **Electrical and Electronics Engineering**
Regulation: **IARE - UG20** Batch: **2021-2025**
Course Name: **Computer Architecture** Course Code: **ACSC24**
Semester: **VI** Target Value: **60% (1.8)**

Attainment of COs:

	Course Outcome	Direct Attainment	Indirect Attainment	Overall Attainment	Observation
CO1	Illustrate the structure, characteristics of computer systems and the various functional units for understanding the components of computers	1.40	2.20	1.6	Not Attained
CO2	Demonstrate the computer languages, machine, symbolic and assembly levels for understanding execution of program	1.00	2.20	1.2	Not Attained
CO3	Make use of the number systems, their representations and conversion for the usage of instructions in digital computers	0.00	2.20	0.4	Not Attained
CO4	Summarize the register transfer language, represent memory and Arithmetic/ Logic/ Shift operations for implementation of micro operations	0.30	2.20	0.7	Not Attained
CO5	Identify the basics of hardwired and micro-programmed control of the CPU which generates the control signals to fetch and execute instructions	1.40	2.20	1.6	Not Attained
CO6	Compare different types of addressing modes for specifying the location of an operand	0.70	2.20	1	Not Attained

Action Taken Report: (To be filled by the concerned faculty / course coordinator)

CO1: Assignments


CO2: Guest lectures

CO3: Extra classes

CO4: Tutorial classes

CO5: Assignments

CO6: Assignments


Course Coordinator


Mentor


Head of the Department

Head of the Department
Electrical and Electronics Engineering
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Dundigal, Hyderabad-500 043.