



INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)

Dundigal, Hyderabad - 500043, Telangana

ELECTRICAL AND ELECTRONICS ENGINEERING

ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT

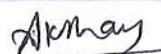
Name of the faculty: Mr. AKSHAYA BHIMASHANKAR K Department: Electrical and Electronics Engineering
Regulation: IARE - UG20 Batch: 2021-2025
Course Name: Microprocessors and Microcontrollers Course Code: AECC19
Semester: V Target Value: 70% (2.1)

Attainment of COs:

	Course Outcome	Direct Attainment	Indirect Attainment	Overall Attainment	Observation
CO1	Describe the features of intel processors and micro controllers for signal description and architecture	0.60	2.40	1	Not Attained
CO2	Make use of addressing modes and instruction set of target microprocessors and micro controllers for writing efficient assembly language programs	0.30	2.30	0.7	Not Attained
CO3	Make use of addressing modes and instruction set of target microprocessors and micro controllers for writing efficient assembly language programs	0.90	2.40	1.2	Not Attained
CO4	Illustrate the interrupt handling mechanism in microprocessors and micro controllers using interrupt controller.	0.00	2.30	0.5	Not Attained
CO5	Choose an appropriate data transfer scheme and hardware for data transfer between the devices.	0.30	2.40	0.7	Not Attained
CO6	Develop microprocessor and micro controller based applications using appropriate input and output devices	0.30	2.40	0.7	Not Attained

Action Taken Report: (To be filled by the concerned faculty / course coordinator)

- CO1: Assignments will be given on the features of intel processors & microcontrollers for signal description & architecture.
- CO2: Guest lecture will conduct on 8086 microprocessor instruction sets to write assembly language programs.
- CO3: Tutorial classes will be conduct on 8051 microcontrollers.
- CO4: Assignment will be given on Interrupt handling mechanisms.
- CO5: Guest lecture will conduct on microprocessor & microcontroller based applications.
- CO6: Remedial classes will conduct on data transfer scheme & hardware for data transfer b/w the devices.


Course Coordinator


Mentor


Head of the Department

Head of the Department
Electrical and Electronics Engineering
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