



INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)

Dundigal, Hyderabad - 500043, Telangana

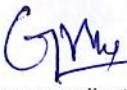
ELECTRICAL AND ELECTRONICS ENGINEERING ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT

Name of the faculty:	Mr. G VISWANATH	Department:	Electrical and Electronics Engineering
Regulation:	IARE - UG20	Batch:	2021-2025
Course Name:	Power System Simulation Laboratory	Course Code:	AEEC45
Semester:	VII	Target Value:	60% (1.8)

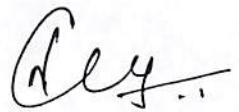
Attainment of COs:

	Course Outcome	Direct Attainment	Indirect Attainment	Overall Attainment	Observation
CO1	Demonstrate the programming concepts of simulation tools for obtaining parameters of a typical transmission line and modelling	3.00	0.00	3	Attained
CO2	Illustrate the formation of bus admittance matrices by adding one element at a time for load flow studies	3.00	0.00	3	Attained
CO3	Interpret the symmetrical and unsymmetrical faults for transmission lines using digital simulation	3.00	0.00	3	Attained
CO4	Evaluate the transient response using numerical methods in RLC circuit and infinite bus systems	3.00	0.00	3	Attained
CO5	Analyze the transformer inrush current for unbalanced three phase parameters	3.00	0.00	3	Attained
CO6	Apply Loadflow methods to get information regarding powerflow	3.00	0.00	3	Attained

Action Taken Report: (To be filled by the concerned faculty / course coordinator)


Course Coordinator


Mentor


Head of the Department

Head of the Department
Electrical and Electronics Engineering
INSTITUTE OF AERONAUTICAL ENGINEERING
Dundigal, Hyderabad-500 043.