INSTITUTE OF AERONAUTICAL ENGINEERING
(Autonomous)
Dundigal, Hyderabad - 500043, Telangana

ELECTRICAL AND ELECTRONICS ENGINEERING

ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT

Name of the faculty:	**Dr. NARENDER REDDY KEDIKA**	Department:	**Electrical and Electronics Engineering**
Regulation:	**IARE - R20**	Batch:	**2022-2026**
Course Name:	**Electrical Circuits Laboratory**	Course Code:	**AEEC03**
Semester:	**II**	Target Value:	**70% (2.1)**

Attainment of COs:

	Course Outcome	Direct Attainment	Indirect Attainment	Overall Attainment	Observation
CO1	Calculate the source resistance, current and voltage using Ohm's, Kirchhoff's laws, mesh and nodal analysis techniques in electrical circuits.	0.60	0.00	0.6	Not Attained
CO2	Analyze the alternating quantities for different periodic wave forms.	0.60	0.00	0.6	Not Attained
CO3	Apply network theorems for complex circuits and verify the current, voltage and power in DC networks.	0.60	0.00	0.6	Not Attained
CO4	Validate the resistance, inductance and power consumed by passive loads using digital simulation.	0.60	0.00	0.6	Not Attained
CO5	Analyze the concepts of impedance, admittance hybrid and transmission parameters of a two port network.	0.60	0.00	0.6	Not Attained
CO6	Determine the resonant frequency, quality factor and bandwidth of the RLC circuits.	0.60	0.00	0.6	Not Attained

Action Taken Report: (To be filled by the concerned faculty / course coordinator)

CO1: Extra labs conducted

CO2: Video lectures done

CO3: More practical explanation given

CO4: Extra labs taken

CO5: Video content done

CO6: Lab repeated

Course Coordinator

Mentor

Head of the Department