

INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous) Dundigal, Hyderabad - 500 043

ELECTRONICS AND COMMUNICATION ENGINEERING

ATTAINMENT OF COURSE OUTCOME- ACTION TAKEN REPORT

Name of the Faculty:	Dr. S China Venkateswarlu	Department:	M.Tech- EMBEDDED SYSTEMS	
Regulation:	R18	Branch:	2020-2022	
Course Name:	Micro Controllers And Programmable	Course Code:	BESB02	
	Digital Signal Processing			
Semester:	I	Target Value:	60% (1.8)	

Attainment of COs:

A STATE	Course Outcome	Direct	Indirect	Overall	Observations
		Attainment	Attainment	Attainment	
CO1	Illustrate the Internal architecture and memory operations of ARM Cortex M3 processor for interfacing microprocessor applications	3	1.9	2.8	Attainment target reached
CO2	Analyze Exceptions handler mechanism to minimize interrupt latency using Nested Vectored Interrupt Controller	3	2.1	2.8	Attainment target reached
CO3	Construct the high level of integration in embedded applications using LPC 17XX Microcontroller	1.6	1.9	1.7	Attainment target is not yet reached
CO4	Demonstrate various computational building blocks of programmable DSP architectures using interfacing of memory and I/O peripherals	1.3	1.7	1.4	Attainment target is not yet reached
CO5	Identify the CPU architecture, peripherals, and development tools for the TMS320C6000 digital signal processors	2	1.9	2	Attainment target reached
CO6	Develop the application for digital signal processing using code composer studio tool	3	1.5	2.7	Attainment target reached

Action Taken Report: (To be filled by the concerned faculty / course coordinator)

CO3: Giving assignments and conducting tutorials on use of use of the MCUXpresso IDE for LPC 17xx microcontroller. CO4: Conducting Guest lectures on universal asynchronous receiver-transmitter concept of serial transmission of digital information.

Course Coordinator

Mentor

Professor & Head
Electronics & Communication Engineering
Institute of Aeronautical Engineering
Dundigal, Hyderabad- 500 043. T.S.