

INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)

Dundigal, Hyderabad - 500 043

INFORMATION TECHNOLOGY

ATTAINMENT OF COURSE OUTCOME – ACTION TAKEN REPORT

Name of the faculty:	K. Linga Swamy	Department:	IT	
Regulation:	IARE - R16	Batch:	2016 - 2020	
Course Name:	Fundamentals of Electrical and Electronics Engineering II-Semester	Course Code:	AEE001	
Semester:		Target Value:	60% (1.8)	

Attainment of COs:

Course Outcome		Direct attainment	Indirect attainment	Overall attainment	Observation
	Solve complex electrical circuits by applying network reduction techniques for reducing into a simplified circuit.	0.9	2.1	1.1	Attainment target is not yet reached
	Make use of various network theorems and graph theory for simplifying complex electrical networks.	0.9	2.0	1.1	Attainment target is not yet reached
CO3	Define basic nomenclature of single phase AC circuits for obtaining impedance, admittance of series and parallel circuits.	0.6	2.0	0.9	Not Attainment target is not yet reached
CO4	Interpret the power factor in single phase circuits with various combination of network elements for computing active and reactive power.	0.9	2.0	1.1	Attainment target is not yet reached
CO5	Apply the PN junction characteristics for the doide applications such as switch and rectifier.	0.9	2.0	1.1	Attainment target is not yet reached
CO6	Extend the biasing techniques for bipolar and uni-polar transistor amplifier circuits considering stability condition for establishing a proper operating point.	0.9	2.0	1.1	Attainment target is not yet reached

Action taken report: (To be filled by the concerned faculty / course coordinator)

- CO 1: Need to solve more problems on complex electrical circuits by applying network reduction techniques.
- CO 2: Delivering more lectures on network theorems and graph theory.
- CO 3: Delivering more lectures on basic nomenclature of single phase AC circuits.
- CO 4: Need to conduct guest lectures on power factor in single phase circuits with various combination of network elements.
- CO 6: Need to conduct guest lectures on biasing techniques for bipolar and uni-polar transistor amplifier circuits.

Course Coordinator