



INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)

Dundigal, Hyderabad-500043

INFORMATION TECHNOLOGY

ATTAINMENT OF COURSE OUTCOME-ACTION TAKEN REPORT

Name of the faculty:	Dr.V.Chandra Jagan Mohan	Department:	IT
Regulation:	IARE-R16	Batch:	2017 -2021
Course Name:	Fundamentals of Electrical and Electronics Engineering	Course Code:	AEE001
Semester:	II-Semester	Target Value:	60% (1.8)

Attainment of COs:

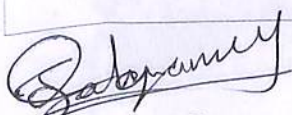
	Course Outcome	Direct attainment	Indirect attainment	Overall attainment	Observation
CO1	Solve complex electrical circuits by applying network reduction techniques for reducing into a simplified circuit.	1.6	2.3	1.7	Attainment target is not yet reached
CO2	Make use of various network theorems and graph theory for simplifying complex electrical networks.	1.6	2.4	1.8	Attainment target is reached
CO3	Define basic nomenclature of single phase AC circuits for obtaining impedance, admittance of series and parallel circuits.	0.9	2.3	1.2	Attainment target is not yet reached
CO4	Interpret the power factor in single phase circuits with various combination of network elements for computing active and reactive power.	1.3	2.4	1.5	Attainment target is not yet reached
CO5	Apply the PN junction characteristics for the diode applications such as switch and rectifier.	2.3	2.3	2.3	Attainment target is reached
CO6	Extend the biasing techniques for bipolar and uni-polar transistor amplifier circuits considering stability condition for establishing a proper operating point.	3.0	2.4	2.9	Attainment target is reached

Action taken report: (To be filled by the concerned faculty/course coordinator)


CO 1: Need to solve problems on complex electrical circuits by applying network reduction techniques.

CO 3: Delivering more lecture on basic nomenclature of single phase AC circuits.

CO 4: Need to conduct guest lectures on power factor in single phase circuits with various combination of network elements for computing active and reactive power.


Course Coordinator


Mentor


HOD