INSTITUTE OF AERONAUTICAL ENGINEERING
(Autonomous)
Dundigal, Hyderabad - 500043, Telangana

INFORMATION TECHNOLOGY
ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT

Name of the faculty:	**Ms. B LAKSHMI PRASANNA**	Department:	**Information Technology**
Regulation:	**IARE - R18**	Batch:	**2019-2023**
Course Name:	**Microprocessors and Interfacing**	Course Code:	**AECB55**
Semester:	**V**	Target Value:	**60% (1.8)**

Attainment of COs:

	Course Outcome	Direct attaiment	Indirect attaiment	Overall attaiment	Observation
CO1	Outline the functional components of microprocessors and microcontrollers for understanding the operation of architectures	3.00	2.50	2.9	Attained
CO2	Make use of addressing modes and instruction set of target microprocessors and microcontrollers for writing an assembly language programs to perform a task.	1.30	2.50	1.5	Not Attained
CO3	Demonstrate the internal architecture and modes of operation of peripheral devices for interfacing memory and I/O devices.	0.90	2.50	1.2	Not Attained
CO4	Illustrate the interrupt handling mechanism in microprocessors and microcontrollers using interrupt controller.	0.60	2.50	1	Not Attained
CO5	Choose an appropriate data transfer scheme and hardware for data transfer between the devices	0.90	2.50	1.2	Not Attained
CO6	Develop microprocessor and microcontroller based applications using necessary input and output devices.	0.90	2.50	1.2	Not Attained

Action Taken:

CO2: Need to discuss more assembly language programs on addressing modes and instruction set of target microprocessors and microcontrollers.

CO3: Need to discuss modes of operation of peripheral devices for interfacing memory and I/O devices with examples.

CO4: Need to conduct seminar on interrupt handling mechanism in microprocessors.

CO5: Need to discuss data transfer scheme and hardware for data transfer between the devices.

CO6: Need to discuss more number of microprocessor and microcontroller based applications.

Course Coordinator

Mentor

Head of the Department