



Dundigal, Hyderabad - 500043, Telangana

INFORMATION TECHNOLOGY

ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT

Name of the faculty:	Mr. K DEVENDER REDDY	Department:	Information Technology	
Regulation:	IARE - R18	Batch:	2019-2023	
Course Name:	Fundamental of Electrical Engineering	Course Code:	AEEB01	
Semester:	I	Target Value:	60% (1.8)	

Attainment of COs:

Course Outcome		Direct attaiment	Indirect attaiment	Overall attaiment	Observation
CO1	Know the fundamental concepts of electric circuits for computing voltage and current relationship of passive elements.	0.70	2.50	1.1	Not Attained
CO2	Solve complex electrical circuits by applying network reduction techniques for reducing into a simplified circuit.	1.60	2.50	1.8	Attained
CO3	Make use of various network theorems for simplifying complex electrical networks.	0.90	2.60	1.2	Not Attained
CO4	Define basic nomenclature of single phase AC circuits for obtaining impedance, admittance of series and parallel circuits.	2.30	2.50	2.3	Attained
CO5	Interpret the power factor in single phase circuits with various combination of network elements for computing active and reactive power.	0.90	2.50	1.2	Not Attained
CO6	Explain formation of incident, cut-set and tie set matrices using which characteristics of electrical circuits can be studied.	1.60	0.00	1.3	Not Attained

Action Taken:

CO1: Need to discuss more problems for computing voltage and current relationship of passive elements.

CO3: Need to solve more network theorems for simplifying complex electrical networks.

CO5: Need to discuss single phase circuits with various combination of network elements with examples.

CO6: Need to provide characteristics of electrical circuits with examples.

Course Coordinator

Mentor

Head of the Department 49 College