

INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous) Dundigal, Hyderabad - 500 043 Department of Electronics and Communication Engineering

Attainment of Program Outcomes (POs) and Program Specific Outcomes (PSOs) of 2019 - 2023 batch (IARE - R18)

Subject Code	Course Title	P01	P02	P03	P04	P05	P06	P07	P08	P09	P010	P011	P012	PS01	PSO2	PSO3
AHSB01	English										2.90					
AHSB02	Linear Algebra and Calculus	2.90	2.90													
AHSB04	Waves and Optics	2.60	2.60	2.50										1.80		
AHSB08	English Language and Communication Skills Laboratory									3.00	3.00					
AHSB10	0 Engineering Physics Laboratory				2.30											2.30
AMEB02	2 Engineering Graphics and Design Laboratory		2.30		2.30											2.30
AHSB11	Mathematical Transform Techniques		2.20		2.90									2.40		
ACSB01	Programming for Problem Solving		2.10	1.20		2.70							2.10	1.70		2.10
AHSB09	Engineering Chemistry Laboratory	3.00	3.00													
ACSB02	Programming for Problem Solving Laboratory	2.00	2.00	2.00		2.00					2.00		2.00			
AMEB01	Workshop Manufacturing Practices Laboratory	2.00		2.00		2.00				2.00	2.00		2.00			2.00
AHSB05	Complex Analysis and Special Functions	1.90	1.90		2.00											
AECB06	Electronic Devices and Circuits	1.30	1.30	1.40							1.30			1.70		
AECB07	Digital System Design	2.70	2.70	2.70	2.70						2.70				2.80	
AECB08	Probability Theory and Stochastic Process	1.70	1.80	1.90							1.70					1.70
ACSB03	Data Structures	2.70	2.70	2.70	2.70	2.80					2.80		2.80	2.80	2.80	2.80
AECB10	Digital System Design Laboratory	2.30	2.30	2.30		2.30				2.30	2.30				2.30	

AECB09	Electronic Devices and Circuits Laboratory		2.30			2.30				2.30			2.30		
ACSB05	5 Data Structures Laboratory		3.00	3.00	3.00	3.00	3.00	3.00	3.00	3.00		3.00			
AECB11	Analog and Pulse Circuits			1.80						1.80				1.90	
AECB12	Analog Communications	2.80	2.80	2.80											2.80
AECB13	Electromagnetic Waves and Transmission Lines	1.60	1.60	0.70											1.40
AECB14	Signals And Systems	1.20	1.60	1.10		1.30				1.10			1.40		
AEEB16	Control Systems	2.80	2.70	2.90						2.80			2.90		
AECB15	5 Analog And Pulse Circuits Laboratory		3.00	3.00	3.00	3.00			3.00	3.00			3.00		
AECB16	5 Analog Communications Laboratory		2.30			2.30			2.30	2.30		2.30			2.30
AECB17	Signals and Systems Laboratory		2.30		2.30	2.30			2.30			2.30	2.30	2.30	
AHSB14	Business Economics and Financial Analysis		2.60					2.80	2.80		2.60				
AECB18	Antennas and Wave Propagation	1.10	1.10	1.20						1.10					1.30
AECB19	Linear And Digital IC Applications	2.30	2.30	2.00	2.30					2.30				2.10	
AECB20	Digital Communications	1.80	1.80	1.50						1.80					1.60
ACSB41	Java Programming	1.70	1.50		1.50	1.50				1.20		1.50	1.50		1.60
AECB32	Electronic Measurements and Instrumentation	1.60	1.50	1.60						1.60				1.60	1.50
ACSB32	Computer Architecture	2.50	2.60							2.50			2.50		
AHSB15	Project Based Learning (Prototype / Design Building)	3.00	3.00	3.00	3.00	3.00			3.00	3.00		3.00	3.00		3.00
AECB21	Linear And Digital IC Applications Laboratory	2.30	2.30	2.30		2.30				2.30				2.30	
AECB22	Digital Communications Laboratory	3.00	3.00			3.00			3.00	3.00					3.00
AECB44	Digital Design Through Verilog	2.80	2.80	2.80	2.80	2.80				2.80				2.80	
AECB23	Digital Signal Processing	1.20	1.20	0.90	0.90	0.40				1.20			1.10		
AECB24	Microprocessors and Microcontrollers	1.40	1.00	1.00						1.20			1.30		

AECB35	Digital Image Processing	1.60	1.70	1.70	2.00						1.60		2.20	1.70		
AECB39	Cellular and Mobile Communications	1.80	1.30	2.60							1.70					1.90
AECB42	Wireless Communications and Networks	2.50	2.40		2.40						2.60					2.40
ACEB53	Disaster Management	1.50					1.50	1.50		1.50						
AHSB16	Research Based Learning (Fabrication / Model Development)	3.00				3.00				3.00	3.00		3.00	3.00	3.00	3.00
AECB25	Digital Signal Processing Laboratory		3.00	3.00		3.00				3.00	3.00			3.00		
AECB26	.6Microprocessors and Microcontrollers Laboratory3.003.003.003.00								3.00	3.00			3.00			
AECB50	Radar Systems and Processing	ng 2.70 2.80 2.80 2.80									2.70					2.80
AHSB22	Intellectual Property Rights 2.80 2.4		2.80		2.70		2.80		2.80							
AECB27	7 VLSI Design		1.90	1.60	1.90						2.00				1.80	
AECB28	Satellite And Microwave Engineering 1.90 1.90 2.00						1.80					1.70				
AECB48	Information Theory and Coding	2.20	2.10	2.10							2.20			2.20		2.00
AECB29	VLSI Design Laboratory	3.00	3.00			3.00				3.00	3.00		3.00		3.00	
AECB30	Antennas and Microwave Engineering Laboratory		3.00	3.00		3.00				3.00	3.00					3.00
AECB61	Project Work - (Phase - I)	3.00	3.00	3.00	3.00	3.00	3.00		3.00	3.00	3.00	3.00	3.00	3.00	3.00	3.00
AECB52	Real Time Systems 2.30 2.30 2.30 2.30		2.30					2.30			2.30					
AECB62	Project Work - (Phase - II)	3.00	3.00	3.00	3.00	3.00	3.00	3.00				3.00	3.00	3.00	3.00	3.00
AECB60	50 IOT And Applications		1.10	2.30		1.50								2.00		
	Direct Attainment Value	2.3	2.3	2.2	2.4	2.5	2.7	2.3	2.9	2.7	2.3	2.9	2.5	2.3	2.5	2.3

Overall Attainment

C No	Assessment Component (Direct + Indirect)		Program Outcomes													PSOs			
5 10.			PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3			
1.	Direct Assessment (CIA + SEE + Course End Survey) (a)			2.2	2.4	2.5	2.7	2.3	2.9	2.7	2.3	2.9	2.5	2.3	2.5	2.3			
2.	Student Program exit surveys (b)			2.2	2.2	2.2	2.3	2.4	2.3	2.4	2.2	2.5	2.3	2.2	2.2	2.2			
3.	Employer surveys (c)	2.4	2.4	2.6	2.4	2.4	2.5	2.4	2.4	2.5	2.3	2.4	2.5	2.4	2.4	2.6			
4.	Alumni Survey (d)	2.5	2.5	2.6	2.6	2.6	2.6	2.6	2.5	2.6	2.5	2.5	2.7	2.3	2.3	2.3			
Overall attainment = a*0.8 + b*0.1 + c*0.05 + d*0.05			2.3	2.2	2.4	2.5	2.6	2.3	2.8	2.7	2.3	2.8	2.5	2.3	2.5	2.3			

Action taken to improve the attainment of Pos and PSOs:

POs	Target Level	Attainment Level	Observations							
PO1: Engine	PO1: Engineering Knowledge: Apply the knowledge of mathematics, science, engineering fundamentals, and an engineering specialization to the solution of complex engineering problems.									
PO1	2.1	2.3	Target is achieved. The following actions were taken to enhance the target level.							
Action 1: A few new FPGA hardware kits have been purchased and their uses have been demonstrated during the theory classes of digital system design course to help the students understand the itilization and functions of the FPGA kits. Action 2: It is advised to monitor the students at regular intervals during the lab session keenly to overcome the difficulty in doing the experiments. Action 3: Expert lectures on signal &system and electronic circuit analysis have been arranged for the students in order to reinforce their knowledge about the application of the subject.										
PO2: Problem Analysis: Identify, formulate, research literature, and analyze complex engineering problems reaching substantiated conclusions using first principles of mathematics, natural sciences, and engineering sciences.										
PO2	2.1	2.3	Target is achieved. The following actions were taken to enhance the target level.							
Action 1: IEEF Action 2: Appl Action 3: In sig	Action 1: IEEE research papers were assigned to students in electromagnetic theory and transmission lines and antennas and wave propagation course to analyze and review the research papers. Action 2: Application oriented problems were solved in electronic circuit analysis, VLSI design, microprocessor and microcontroller and embedded systems to improve logical thinking. Action 3: In signal and systems and digital communication, tutorial sessions were taken for the application of theory to real life problem.									
PO3: Design /of for the public h	development of Solution health and safety, and the	ns: Design solutions for complex engine e cultural, societal, and environmental c	eering problems and design system components or processes that meet the specified needs with appropriate consideration considerations.							
PO3	2.1	2.2	Target is achieved. The following actions were taken to enhance the target level.							
Action 1: Societal and environmental design problems were given as self-study to students in open elective courses. Action 2: In electronic devices and circuit and antennas & wave propagation, assignments were given to students to solve real field design problems. Action 3: Industrial visits are expected to help students gain knowledge on complex engineering problems.										
PO4: Conduct information to	PO4: Conduct Investigations of Complex Problems: Use research-based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions.									
PO4	2.1	2.4 Target is achieved. The following actions were taken to enhance the target level.								

Action 1: Stude	Action 1: Students are encouraged to participate in building small experimental kits which can be used to teach future students and their juniors. This gives them an initial exposure to hardware									
implementation	mplementation and experimentation, thereby enabling better productivity during final year project									
PO5: Modern with an underst	PO5: Modern Tool usage: Create, select, and apply appropriate techniques, resources, and modern engineering and IT tools including prediction and modelling to complex engineering activities with an understanding of the limitations.									
PO5	2.1	2.5	Target is achieved. The following actions were taken to enhance the target level.							
Action 1: Complex design problem using modern tool were given as assignment to the students in digital signal processing, digital image processing and embedded system design courses.										
Action 2: Work	Action 2: Workshops were conducted on several electronics and communication engineering software packages like PCB Design, antenna design(HFSS), Vivoda and Cadence.									
Action 3: Han processing court	Action 3: Hands-on sessions on MATLAB-simulink engineering tool were given to students to understand the concept of analog communication, digital communication and digital signal processing courses.									
PO6: The Engineer and Society: Apply reasoning informed by the contextual knowledge to assess societal, health, safety, legal and cultural issues and the consequent responsibilities relevant to the professional engineering practice.										
PO6	2.1	2.7	Target is achieved. The following actions were taken to enhance the target level.							
Action 1: To un Besides this son	nderstand the safety con- ne mandatory humanitie	cerns and social aspects, student industress courses ensure that students are repea	ry visits are encouraged to expand their practical knowledge with the effect of improved practices in engineering. Atedly reminded of their social responsibilities as electronics engineers.							
PO7: Environ sustainable dev	ment and Sustainabilit elopment.	y: Understand the impact of the profes	ssional engineering solutions in societal and environmental contexts, and demonstrate the knowledge of, and need for							
PO7	2.1	2.3	Target is achieved. The following actions were taken to enhance the target level.							
Action 1: Students are encouraged to indulge in projects, in which global and environmental issues are improved, with respect to consumption of energy and utilization of renewable energy resources. Action 2: Awareness program on effect of electronics circuit to nature will be given to promote a sustainable environment. Action 3: Proper guidance were given to the students to implement renewable energy projects using optimized material that would guarantee sustainable development.										
PO8: Ethics: A	PO8: Ethics: Apply ethical principles and commit to professional ethics and responsibilities and norms of the engineering practice.									
PO8	2.1	2.9 Target is achieved. The following actions were taken to enhance the target level.								

Action 1: Gues Action 2: Onlin Action 3: Stude	Action 1: Guest lectures were arranged on topics related to professional ethics / value based education. Action 2: Online video links on ethical principles in electronics subjects were shared to students. Action 3: Students were encouraged to get their major project and internship reports for plagiarism check to ensure proper practice of professional ethics.									
PO9 : Individı	'O9 : Individual and Team Work: Function effectively as an individual, and as a member or leader in diverse teams, and in multidisciplinary settings.									
PO9	2.1	2.7	Target is achieved. The following actions were taken to enhance the target level.							
Action 1: Instit groups, helping Action 2: Seve Action 3: Stude	Action 1: Institute holds cultural programs and alumni meets where students are encouraged to volunteer as organizers. This provides them with a platform to work as individuals as well as in groups, helping students groom their skills like leadership and team spirit. Action 2: Several students' professional chapter activities were organized to demonstrate their abilities as team members in a group. Action 3: Students were motivated to work in a team in project work/Internship.									
PO10: Communication: Communicate effectively on complex engineering activities with the engineering community and with society at large, such as, being able to comprehend and write effective reports and design documentation, make effective presentations, and give and receive clear instructions.										
PO10	2.1	2.3	Target is achieved. The following actions were taken to enhance the target level.							
Action 1: Stude Action 2: Stude Action 3: Soft presentations ar	Action 1: Students were given training to write effective reports and make effective presentations on projects undertaken. Action 2: Students were encouraged to participate in class room presentations and national/international conferences/seminars/symposia/ hackathon / ideathon. Action 3: Soft skills training is imparted to students to enhance various aspects of communication/technical talks by group discussions, presentations and new learning outcomes. Regular seminars and presentations are held to help students communicate technical ideas well									
PO11: Project a team, to mana	t Management and Fin age projects and in multi	ance: Demonstrate knowledge and under disciplinary environments.	erstanding of the engineering and management principles and apply these to one's own work, as a member and leader in							
PO11	2.1	2.9	Target is achieved. The following actions were taken to enhance the target level.							
Action 1: Stud Action 2: Awa students are exp	Action 1: Students were encouraged to do multidisciplinary project involving allied departments. Action 2: Awareness is created among the student regarding the management principles and managing projects. Also, with many management-based recruiters offering placements in the campus, students are expected to realize the importance of management in engineering									
PO12:Life-lon	PO12:Life-long Learning: Recognize the need for, and have the preparation and ability to engage in independent and life-long learning in the broadest context of technological change.									
PO12	PO122.12.5Target is achieved. The following actions were taken to enhance the target level.									

Action 1: The recent technology like FIN-FET technology, 5G wireless communication, cyber security, machine learning, artificial intelligence, will be introduced to the students. Action 2: Students were motivated to take up NPTEL certification on python, networking, RF design, communication, VLSI related subjects. Action 3: Students were motivated to pursue higher studies in areas of wireless communication, RF design, semiconductor technology in premier institution.									
PSO1: Build embedded software and digital circuit development platform for robotics, embedded systems and signal processing applications.									
PSO1	2.1	2.3	Target is achieved. The following actions were taken to enhance the target level.						
Action 1: Stuc Action 2: Stuc Action 3: Stuc	Action 1: Students are motivated to take up the real-life problems during their project work so that they can design, analyze, and find solution which gives exposure to latest technologies. Action 2: Students were given design-oriented activities in emerging fields of electronics and communication engineering Action 3: Students were encouraged to take up MOOC courses as part of co-curricular activities.								
PSO2: Focus of	PSO2: Focus on the Application Specific Integrated Circuit (ASIC) Prototype designs, Virtual Instrumentation and System on Chip (SOC) designs.								
PSO2	2.1	2.5	Target is achieved. The following actions were taken to enhance the target level.						
Action 1: Stu Action 2: Stud	idents are motivated to take	ake up the real-life problems during th e up online NPTEL courses on basic en	eir project work so that they can design, analyze, and find solution which gives exposure to latest technologies gineering hardware and programming languages.						
PSO3: Make u	ise of high frequency stru	ucture simulator for modeling and evalu	ating the patch and smart antennas for wired and wireless communication applications.						
PSO3	32.12.3Target is achieved. The following actions were taken to enhance the target level.								
Action 1: Han Action 2: Shoi Action 3: Stud	Action 1: Hands on workshop were conducted from industry experts on latest hardware and software for getting real time exposure. Action 2: Short term training program were conducted on program specific courses Action 3: Students were motivated to take up industry related project to get understanding of advanced industry tools.								





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