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| | INSTITUTE OF AERONAUTICAL ENGINEERING | G |
| | B.Tech V Semester End Examinations (Regular), February – 2021 | |
| | Regulation: IARE–R18 COMPILER DESIGN | |
| Time: 3 Hours(Common to CSE IT)Max Marks: 70 | | |
| | Answer any Four Questions from Part A Answer any Five Questions from Part B | |
| | $\mathbf{PART} - \mathbf{A}$ | |
| 1. | Explain language processing system with neat diagram. | [5M] |
| 2. | Explain context free grammar with examples. | [5M] |
| 3. | Write short notes about syntax directed translation. | [5M] |
| 4. | Elucidate in detail about type systems and type expression. | [5M] |
| 5. | Explain principal sources of optimization with examples. | [5M] |
| 6. | How lexical analyzer is constructed using lex? Give an example. | [5M] |
| 7. | Give informative notes on shift reduce parser. Differentiate SLR, CLR, and LALR parser. | [5M] |
| 8 | Write a short notes on abstract syntax tree | [5M] |
| 0. | | |
| 9 | PART – B Show how the DFA is directly converted from an augmented regular expression $(a/b)^*abb$ | [10M] |
| 10 | Solve the given regular expression using Thempson construction | |
| 10. | solve the given regular expression using Thompson construction $i)(a/b)^*abb$ | |
| | $(0/\epsilon).(0/1)^{-0}$ | |
| 11. | Design a predictive parsing table for the grammar. $E \rightarrow E+T$ $T \rightarrow T^*E$ | |
| | $F \rightarrow (E)/id$ and shows the moves for the input string $id+id^*id$. | [10M] |
| 12. | Check whether the following grammar is SLB or not. Explain your answer with reasons | [] |
| | S \rightarrow L=R R | |
| | $L \rightarrow R \mid id$ | |
| | $R \rightarrow L$ | [10M] |
| 13. | Translate the following expression to quadruple, triple and indirect triple- | |
| | $\mathbf{a} = \mathbf{b} \mathbf{x} - \mathbf{c} + \mathbf{b} \mathbf{x} - \mathbf{c}.$ | |
| | | [10M] |
| 14. | Suppose that we have a production $A \rightarrow BCD$ $A \rightarrow BCD$. Each of the four non terminals A, E have two attributes: ss is a synthesized attribute, and ii is an inherited attribute. For each below, tell whether | 3,C,A,B,C, and DD of the sets of rules |
| | i) the rules are consistent with an S-attributed definition | |
| | ii) the rules are consistent with an L-attributed definition, and | |
| | iii) whether the rules are consistent with any evaluation order at all | |
| | a. $A.s=B.i+C.s.A.s=B.i+C.s.$ | |
| | b. $A.s=B.i+C.sA.s=B.i+C.s$ and $D.i=A.i+B.s.D.i=A.i+B.s.$ | |
| | C. A.S=D.S+ \cup .SA.S= \bigcup .S+ \bigcup .S d A s=D i B i=A s+ \bigcup s C i=B s A s=D i B i=A s+ \bigcup s C i=B s and D i=B i+C i D i=D i+ | -Ci [101/] |
| | u. $1.5 - D.1, D.1 - A.5 + O.5, O.1 - D.5, A.0 - D.1, D.1 - A.5 + O.5, O.1 - D.5, Allu D.1 - D.1 + O.1.D.1 = D.1 + O.1 - D.1 + O.1 + O.1 - D.1 + O.1 + O.1 - D.1 + O.1 + O.1 + O.1 + O.1 + O.1 + O.1 - D.1 + O.1 $ | |

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- 15. Develop a quicksort algorithm for reads nine integers into an array a and sorts them by using the concepts of activation tree. [10M]
- 16. Explain the specification of simple type checker for statements, expressions and functions. [10M]
- 17. Explain the following with respect to code generation phase. i) Input to code generator ii) Target program iii) Memory management iv) Instruction selection v) Register allocation vi) Evaluation order [10M]
- 18. Construct the DAG for the following Basic block & explain it.

i) S1:= 4 * iii) S2:= a[S1]iii) S3:= 4 * iiv) S4:= b[S3]v) S5:= s2 * S4vi) S6:= prod + S5vii) Prod:= s6viii) S7:= i+1ix) i := S7if $i \le 20 \text{ goto } (1)$

[10M]

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