| Hall Ticket No                                 | Question Paper Code: BESB06 |
|--|-----------------------------|
| INSTITUTE OF AERONAUTICAL EN                   | IGINEERING                  |
| (Autonomous)                                   |                             |
| M.Tech I Semester End Examinations (Regular) - | January, 2019               |
| Regulation: IARE–R18                           |                             |
| PRINCIPLES OF DISTRIBUTED EMBED                | DED SYSTEMS                 |
|  |                             |

Time: 3 Hours

 $(\mathbf{ES})$ 

Max Marks: 70

# Answer ONE Question from each Unit All Questions Carry Equal Marks All parts of the question must be answered in one place only

## $\mathbf{UNIT} - \mathbf{I}$

| 1. | (a) List out the elements of real time systems. Explain each with neat sketch. | [7M] |
|----|--|------|
|    | (b) Explain in detail to synchronize the external and internal clock.          | [7M] |
| 2. | (a) Brief about internal and external clock synchronization.                   | [7M] |
|    | (b) Write short on the following with neat sketch                              | [7M] |
|    | i. Event triggered   |      |

ii. Time triggered

### $\mathbf{UNIT}-\mathbf{II}$

| 3. | <ul><li>(a) Draw the structure of real time operating system and explain in detail.</li><li>(b) Write shorts notes on the following. i. Task states ii. Periodic task iii. Aperiodic task</li></ul> | [7M]<br>[7M] |
|----|---|--------------|
| 4. | <ul><li>(a) Write a C code to interface LCD with any one processor with neat sketch.</li><li>(b) Briefly explain how to detect error in an RTOS and also express how to rectify it.</li></ul>       | [7M]<br>[7M] |

### $\mathbf{UNIT}-\mathbf{III}$

| [7M]        | 5. (a) Explain in detail about the priority-based scheduling algorithm with example.            |
|-------------|---|
| [7M]        | (b) Express and explain the pre-processing steps need to be done before system design.          |
| ler design. | 6. (a) Briefly describe about verification and validation procedure of an embedded system under |
| [7M]        |   |
| to with ita | (b) Explain in detail about time triggered architecture with any one application illustrate     |

(b) Explain in detail about time triggered architecture with any one application, illustrate with its timing diagram. [7M]

#### $\mathbf{UNIT}-\mathbf{IV}$

| 7. | (a) Explain in detail about the CAN collisions and arbitration.   | [7M]         |
|----|---|--------------|
|    | (b) How to organize object directory in CAN protocol. Express it with any one example.  | [7M]         |
| 8. | <ul><li>(a) List out the mandatory entries of CAN protocol and explain each.</li><li>(b) Write short notes on Electronic Data Sheets(EDS) and editing in CAN Open standard.</li></ul> | [7M]<br>[7M] |
|    | $\mathbf{UNIT} - \mathbf{V}$  |              |

9. (a) Brief about the estimated bandwidth usage in a CAN Open configuration. [7M]
(b) Write short notes on CAN OpenIA Configuration and its standards. [7M]
10. (a) List out the different layers used for CAN communication and explain each layer. [7M]
(b) Discuss in detail about the configuration of a device and the basic information required to configure it. [7M]

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