ANALOG AND DIGITAL ELECTRONICS LABORATORY

		Category	Hours /Week			Credits	Maximum Marks		
AECB04		Core	L	Т	Р	С	CIA	SEE	Tota
			-	-	3	1.5	30	70	100
Contact Classes: Nil		Tutorial Classes: Nil	Practical Classes:			36	Total Classes: 36		
 Impleme Illustrate Illustrate Design a Build the Design a Design a Under Under Under Under Analy Analy Analy Under Realiz Realiz 	nt and study t the concept of and construct of concept of d and analyze the EARNING O stand the pn j stand the zen stand half wa ze input and o ze input and o stand the free stand boolea stand universist stand adder/s stand adder/s	nor gates subtractor to gray conversion and excitation tables er exer	wave and tics. nd voltag with and	l full wa	ve recti tor. t filter.	fiers.			
Expt. 1	PN JUNC	FION DIODE CHARAC							
-		eristics of PN diode and c			ıd dynaı	mic resistar	nce using		
Expt. 2	ZENER D	IODE CHARACTERIS	TICS AN	ND VOI	TAGE	REGULA	TOR		
Verification Hardware.	of V-I charact	eristics of Zener diode an	d perfori	n Zener	diode a	s a Voltage	e regulato	r using	
Expt. 3	HALF WA	WE AND FULL WAVE	RECTI	FIER					

Expt. 4	TRANSISTOR CE CHARACTERISTICS					
Verification of Input and Output characteristics of CE configuration using hardware						
Expt. 5	TRANSISTOR CB CHARACTERISTICS					
Verification of Input and Output characteristics of CB configuration using hardware						
Expt. 6	FREQUENCY RESPONSE OF CE AMPLIFIER					
Determine the Gain and Bandwidth of CE amplifier using hardware.						
Expt. 7	BOOLEAN EXPRESSIONS USING GATES					
Realization of Boolean Expressions using Gates						
Expt. 8	UNIVERSAL GATES					
Design and realization of logic gates using universal gates						
Expt. 9	NAND / NOR GATES					
Generation of clock using NAND / NOR gates						
Expt. 10	ADDER/ SUBTRACTOR					
Design a 4 – bit Adder / Subtractor						
Expt. 11	BINARY TO GRAY CONVERTER					
Design and realization of a 4 – bit gray to Binary and Binary to Gray Converter						
Expt. 12	TRUTH TABLES AND EXCITATION TABLES					
Verification of truth tables and excitation tables						
Expt. 13	SHIFT REGISTER					
Design and realization of an 8 bit parallel load and serial out shift register using flip-flops						
Expt. l4	MULTIPLEXER					
Design and realization of 8x1 using 2x1 MUX						
Expt. 15	2 BIT COMPARATOR					
Design and realization of 2 bit comparator						

Reference Books:

- 1. Jacob Millman, Herbert Taub, Mothiki S PrakashRao, -Pulse Digital and Switching Waveforms, Tata McGraw-Hill, 3rd Edition, 2008. 2. David A. Bell, —Solid State Pulse Circuits^{II}, PHI, 4th Edition, 2002.
- 3. D Roy Chowdhury, —Linear Integrated Circuits^{||}, New Age International (p) Ltd, 2nd Edition, 2003.
- 4. Ramakanth A. Gayakwad, -Op-Amps & linear ICsl, PHI, 3rd Edition, 2003.

Web References:

- 1. http://www.tedpavlic.com/teaching/osu/ece327/
- 2. http://www.ee.iitkgp.ac.in
- 3. http://www.citchennai.edu.in
- 4. http://american.cs.ucdavis.edu/academic/ecs154a.sum14/postscript/cosc205.pdf
- 5. http://www.ece.rutgers.edu/~marsic/Teaching/DLD/slides/lec-1.pdf

Course Home Page: