



INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)

Dundigal, Hyderabad - 500 043

ELECTRICAL AND ELECTRONICS ENGINEERING

QUESTION BANK

Course Name	:	MICROPROCESSORS AND INTERFACING DEVICES
Course Code	:	A60430 (R15)
Class	:	III-B. Tech II Semester
Branch	:	ELECTRICAL AND ELECTRONICS ENGINEERING
Year	:	2017 – 2018
Course Coordinators	:	Mr.R Mahendhar Reddy, Associate Professor
Course Faculty	:	Mr.R Mahendhar Reddy, Associate Professor

OBJECTIVES

To meet the challenge of ensuring excellence in engineering education, the issue of quality needs to be addressed, debated and taken forward in a systematic manner. Accreditation is the principal means of quality assurance in higher education. The major emphasis of accreditation process is to measure the outcomes of the program that is being accredited.

In line with this, Faculty of Institute of Aeronautical Engineering, Hyderabad has taken a lead in incorporating philosophy of outcome based education in the process of problem solving and career development. So, all students of the institute should understand the depth and approach of course to be taught through this question bank, which will enhance learner's learning process.

S. No	QUESTION	Blooms Taxonomy Level	Course Outcome
UNIT-I 8086 ARCHITECTURE PART-A (SHORT ANSWER QUESTIONS)			
1.	Define Microprocessor and mention the power supply & clock frequency of 8086	Understand	1
2.	List out few applications of microprocessor-based system.	Understand	1
3.	Explain hardware interrupts in 8086	Understand	1
4.	Discuss about pipelining.	Understand	1
5.	List out the general purpose registers of 8086.	Understand	1
6.	List out the interrupts of 8086.	Understand	1
7.	List out features of 8086 microprocessor.	Understand	1
8.	Name the functional units of 8086 microprocessor	Understand	1
9.	Explain the functions of an accumulator.	Understand	1

S. No	QUESTION	Blooms Taxonomy Level	Course Outcome
10.	Explain why 8086 internal architecture is divided into BIU & EU?	Understand	1
11.	Discuss the functions of BIU.	Understand	1
12.	Discuss the functions of EU.	Understand	1
13.	Name the special function registers of 8086.	Remember	1
14.	Describe the flag register of 8086.	Understand	1
15.	Discuss how physical address is generated in 8086.	Understand	1
16.	List out advantages of memory segmentation.	Remember	1
17.	Evaluate the physical address, if base address is 5200H & offset address is 4510H.	Remember	1
18.	Explain the physical memory organization of 8086.	Understand	1
19.	List the operating modes of 8086.	Remember	1
20.	List the minimum mode signals.	Remember	1
21.	List the maximum mode signals.	Remember	1
22.	Explain ALE, BHE/S7, DEN, DT/R.	Understand	1
23.	Explain READY, MN/ \overline{MX} , HOLD and HLDA.	Understand	1
24.	Indicate the interrupts of 8086.	Understand	1
25.	Write about the sources of interrupts.	Understand	1
26.	Explain how many addresses can generate by 16 bit address bus.	Remember	1
PART-B (LONG ANSWER QUESTIONS)			
1	Explain the architecture of 8086 with neat diagram.	Understand	1
2	Compare 8085 & 8086 microprocessors.	Remember	1
3	Write the size of 8086 instruction queue and explain how does queue speed up the processing.	Remember	1
4	Explain general purpose & special purpose registers of 8086.	Understand	1
5	Explain the flag register of 8086.	Understand	1
6	Discuss memory segmentation & mention its advantages.	Understand	1
7	Describe the 8086 microprocessor pin-diagram.	Understand	1
8	Discuss about the functions of the following pins. a) TEST b) RQ/GT0 & RQ/GT1 c) QS0 & QS1 d) S0, S1, S2	Understand	1
9	Explain minimum mode configuration of 8086 microprocessor.	Understand	1
10	Explain minimum mode control signals of 8086.	Understand	1
11	Explain maximum mode configuration of 8086 microprocessor.	Understand	1

S. No	QUESTION	Blooms Taxonomy Level	Course Outcome
12	Explain maximum mode control signals of 8086.	Understand	1
13	Explain the read & write timing diagrams for maximum mode	Understand	1
14	Explain the read & write timing diagrams for minimum mode configuration.	Understand	1
15	Describe the function of the following signals. a) NMI b) $\overline{\text{LOCK}}$ c) $\overline{\text{TEST}}$ d) RESET	Understand	1
16	Describe the interrupts of 8086.	Understand	1
PART-C (ANALYTICAL QUESTIONS)			
1	Calculate the effective address & physical address of the following instructions. (a) IMUL AX, [BP + BX - 8D] (b) SBB AL, ES:[SI + 5D] PUSH AX (d) AND AH, [SI + 42D] (e) CMPSB (f) CMPB DX, [SI] Assume CS = 5000H, DS = 8000H, SS = A000H, ES = B000H, SI = 2000H, DI = 6000H, BP = 1002H, SP = 0002H, AX = 0000H, BX = 5200H, CX = 2000H.	Apply	1
2	Calculate the physical address is represented by i) 4370:561EH ii) 7A32:0028H	Apply	1
3	Evaluate the physical address of the top of the stack? If the stack segment register contains 3000h and the stack pointer register contains 8434H.	Evaluate	1
4	Identify the memory address of the next instruction executed by the microprocessor, when operated in the real mode, for the following CS:IP combinations: a) CS = 1000H and IP = 2000H b) CS= 2000h and IP=10000h	Understand	1
UNIT-II			
INSTRUCTION SET AND ASSEMBLY LANGUAGE PROGRAMMING OF 8086			
PART-A (SHORT ANSWER QUESTIONS)			
1	Define addressing mode. Write the names of 8086 addressing modes.	Remember	2
2	Discuss about the immediate addressing mode of 8086 with example.	Understand	2
3	Explain the use of relative plus addressing mode.	Remember	2
4	Explain the instruction formats in 8086.	Understand	2
5	Define an instruction, opcode and operands.	Remember	2

S. No	QUESTION	Blooms Taxonomy Level	Course Outcome
6	Discuss the following instructions of 8086. a) ADC b) AAS c) IMUL d) CBW	Understand	2
7	Explain the meaning of following instructions. a) MOV BX,[1234H] b) ADC AX,BX	Understand	2
8	Discuss the instructions which will not affect flag registers.	Understand	2
9	Explain ESC, NOP and LOCK instructions of 8086.	Understand	2
10	Explain PUSH & POP instructions.	Understand	2
11	Explain XLAT instructions.	Understand	2
12	Describe the 16- bit registers are present in 8086.	Understand	2
13	Distinguish the TEST & logical AND instructions.	Understand	2
14	Explain stack pointer and instruction pointer.	Understand	2
15	Distinguish the ADD & INC instructions.	Understand	2
16	Explain IN & OUT instructions.	Understand	2
17	Write any two address transfer instructions.	Remember	2
18	Give two conditional jump instructions with an example.	Understand	2
19	Explain NEG, COMP instructions.	Understand	2
20	Define assembler directive. Give any two examples.	Remember	2
21	Explain DAA and AAA instructions.	Understand	2
22	Explain MUL and DIV instructions.	Understand	2
23	Explain rotate instructions.	Understand	2
24	Explain Shift instructions.	Understand	2
25	Define procedure and macro.	Understand	2
PART-B(LONG ANSWER QUESTIONS)			
1	Discuss the instructions formats of 8086.	Understand	2
2	Explain the various addressing modes of 8086 with examples.	Understand	2
3	Explain arithmetic instructions of 8086 examples.	Understand	2
4	Explain data transfer instructions of 8086 with examples.	Understand	2
5	Distinguish procedures and macros.	Understand	2
6	Write short notes on JUMP instructions with examples.	Remember	2
7	Explain the following instructions: a) WAIT b) HLT c) ESC d) NOP	Understand	2
8	Write the uses of Shift & rotate instructions.	Remember	2
9	Differentiate jump & loop instructions.	Understand	2
10	Write short notes on string instructions.	Remember	2
11	Write an assembly language program to reverse the given string "1,2,3,4,5".	Remember	2
12	Write the logical instructions available in 8086.	Remember	2

S. No	QUESTION	Blooms Taxonomy Level	Course Outcome
PART-C (ANALYTICAL QUESTIONS)			
1	Write an assembly language program to convert unpacked BCD to ASCII.	Create	3
2	Write an assembly language program to sort the given values in ascending order.	Create	3
3	Write an assembly language program to insert a byte into the give array.	Create	3
4	Write a delay loop which produces a delay of 500µsec on an 8086 with 5-MHz clock.	Create	3
5	Write an assembly language program to find factorial of a given number.	Create	3
6	Write an assembly language program to find sum of squares.	Create	3
7	Write an assembly language program to find number of positive & negative numbers from a given array.	Create	3
8	Write an assembly language program to convert ASCII to BCD.	Create	3
9	Write an ALP to find cube of an 8-bit hexadecimal number.	Create	3
10	Write an ALP to display 'IARE MPMC LAB' on the screen.	Create	3
11	Write an ALP to convert a given sixteen bit binary number to its gray equivalent.	Create	3
UNIT-III			
I/O INTERFACING WITH ADVANCED DEVICES			
PART-A (SHORT ANSWER QUESTIONS)			
1	Explain why I/O interface required for 8086.	Understand	4
2	List out the features of the 8255 (PPI).	Remember	4
3	Write how many port lines are present in 8255.	Remember	4
4	Discuss in how many modes 8255 can be operated.	Understand	4
5	Explain BSR mode of operation.	Understand	4
6	Write how many I/O modes of operations present in 8255.	Remember	4
7	What are the internal devices of 8255	Remember	4
8	Discuss the operation of Mode 0 in 8255	Understand	4
9	Write the applications of DAC	Remember	4
10	Discuss the operation of Mode 1 in 8255	Remember	4
11	Discuss the operation of Mode 2 in 8255	Understand	4
12	What is programmable peripheral device	Understand	4
13	Write the control word format for BSR mode.	Remember	4

S. No	QUESTION	Blooms Taxonomy Level	Course Outcome
14	Write the control word format for I/O mode.	Remember	4
15	Write in which mode of I/O operation Bi-directional data transfer takes place explain.	Remember	4
16	Calculate the control port address of 8255 if the base address is FFF0H.	Remember	4
17	Explain the function of handshaking signals.	Understand	4
18	Define Memory mapped I/O.	Understand	4
19	Define I/O mapped I/O.	Understand	4
20	Explain the purpose of CE or CS pin on a memory chip.	Remember	4
21	Write the input for chip select.	Remember	4
22	Discuss the need for ADC.	Understand	4
23	List out the methods available for ADC.	Remember	4
24	Explain key bouncing.	Understand	4
25	The 8255 Programmable Peripheral Interface issued as described below	Understand	4
26	Discuss in an 8086 based system, the maximum number of input output devices can be connected using I/O mapped I/O method is?	Remember	4
27	Explain in BSR mode of 8255 PPI which port can be set or reset	Remember	4
28	Define Programmable interrupt controller?	Understand	4
29	Explain how high power devices are interfaced to 8086 using 8255 PPI	Knowledge	4
30	What is the setting time of DAC 0800	Remember	4
UNIT-III (MID TERM II) I/O INTERFACING WITH ADVANCED DEVICES PART-A (SHORT ANSWER QUESTIONS)			
31	Define interrupt vector table.	Remember	4
32	Define interrupt service routine.	Knowledge	4
33	Distinguish the difference between mask able and non-mask able interrupts?	Understand	4
34	List out the interrupts of 8086.	Understand	4
35	Write the priorities of 8086 interrupts.	Understand	4
36	List out the uses of INT-03H interrupt.	Remember	4
37	List different type of DOS interrupts?	Understand	4
38	List different types of BIOS interrupt?	Understand	4

S. No	QUESTION	Blooms Taxonomy Level	Course Outcome
39	Define PIC	Understand	4
40	Define ICW3 Format	Understand	4
41	Discuss ICW1 & ICW4	Understand	4
42	Define OCW3	Understand	4
43	Explain control word register (CWR). Of 8259	Understand	4
44	Define DMA with example	Understand	4
45	Define Operating modes OF 8257	Understand	4
46	Discuss HOLD & HLDA signals	Understand	4
47	Discuss about DMA operation	Understand	4
48	Explain about Master mode operation of 8259	Understand	4
49	Explain about Slave mode operation of 8259	Understand	4
50	Explain about Polling mode and Burst mode operations of 8259	Understand	4
51	Explain Polling mechanism	Understand	4
52	Explain about Interrupt processing mechanism 8259 with 8086	Understand	4
53	Explain about DACK, INTR signals	Understand	4
54	Explain about DRQ signal	Understand	4
55	Explain how to handle 64 interrupts	Understand	4
56	Explain the basic requirement of Programmable interrupt controller	Understand	4
57	While executing main program, if two or more interrupts occur, then the sequence of appearance of interrupts is called?	Understand	4
58	Discuss once the processor responds to an INTR signal, the IF is automatically	Understand	4
59	Discuss which external interrupt has highest priority	Understand	4
60	Discuss which internal interrupt has highest priority	Understand	4
PART-B (LONG ANSWER QUESTIONS)			
1	Write an ALP to interface stepper motor with 8086.	Remember	4
2	Explain the control word format of 8255 in I/O & BSR mode.	Understand	4
3	Write a program for 8-bit ADC to sample analog input & store the digital value in memory.	Remember	4
4	Write an ALP to generate square wave.	Remember	4
5	Describe the block diagram of successive approximation method and explain.	Understand	4
6	Explain the interfacing diagram of ADC with 8255.	Understand	4
7	Explain how a stepper motor is interfaced to 8086.	Understand	4

S. No	QUESTION	Blooms Taxonomy Level	Course Outcome
8	Explain how a display device interfaced with 8086.	Understand	4
9	Discuss how a 4×4 key board matrix is connected to 8255.	Understand	4
10	Explain how the physical memory of 8086 is organized	Understand	4
11	Explain the interrupt vector table.	Understand	5
12	Write short notes on interrupt service routine.	Remember	5
13	Construct an Interface of two 4k×8 EPROMS & and two 4k×8 RAM chips with 8086. Select suitable memory map.	Remember	5
14	Explain about the programmed I/O & interrupt driven I/O.	Understand	4
15	Explain the interrupt sequence in 8086 system.	Understand	5
16	Write short notes on 5 types of interrupts supported by 8086.	Remember	5
17	Describe the 8259 with neat block diagram.	Understand	5
18	Discuss how 8259 is used for handling interrupts.	Understand	5
19	Explain the advantages of using the DMA	Understand	5
20	Describe the 8257 with neat block diagram.	Understand	6
21	Explain about ICW's of 8259	Understand	6
22	Explain about OCW's of 8259	Understand	6
23	Explain how to interface 8259 with 8086	Understand	6
24	Explain how to interface 8257 with 8086	Understand	6
PART-C (ANALYTICAL QUESTIONS)			
1	Write an Assembly Language Program to rotate the Stepper Motor in Clockwise and Anti-Clockwise.	Apply	4
2	Write ALP to Generate Triangular and Sawtooth Waveforms	Remember	6
3	Write ALP to Generate the Squarewaveform	Remember	6
4	Write ALP to DAC	Remember	6
UNIT-IV			
18051 Real Time Control			
PART-A (SHORT ANSWER QUESTIONS)			
1	Define serial communication	Understand	8
2	Define parallel communication	Understand	8
3	Discuss about TXC (Input terminal)	Understand	8
4	Define simplex communication with example	Understand	8
5	Explain control word register (CWR).	Understand	8
6	Define half duplex communication with example	Understand	8
7	Define full duplex communication with examples	Understand	8

S. No	QUESTION	Blooms Taxonomy Level	Course Outcome
8	Discuss about C/D (Input terminal)	Understand	8
9	Discuss about synchronous communication	Understand	8
10	List out the serial communication standards	Understand	8
11	What are the data transfer schemes	Understand	8
12	Features of 8251 USART	Understand	8
13	Types of control words in 8251	Remember	8
14	Discuss SYNDET/BD terminal	Understand	8
15	Discuss about RXRDY terminal	Understand	8
16	Discuss about Asynchronous communication	Understand	8
17	Classify the various types of serial communications	Understand	8
PART-B (LONG ANSWER QUESTIONS)			
1	Describe the 8251 with neat block diagram.	Understand	8
2	Discuss how 8251 is used for serial communication of data.	Remember	8
3	Explain the advantages of using the USART	Understand	8
4	Discuss the types of serial communication.	Understand	8
5	Explain the interfacing of 8251 with 8086 with necessary circuit diagram	Understand	8
6	Discuss the data transmission standards and their specifications	Understand	8
7	Explain the pin structure of RS232C & also discuss about voltage & current specifications of RS 232C.	Understand	8
8	Describe the logic diagram to convert TTL to RS232C conversion and RS232C to TTL conversion.	Understand	8
9	Explain about IEEE-488	Understand	8
10	Explain about prototype and Trouble shooting	Remember	8
PART-C (ANALYTICAL QUESTIONS)			
1	Illustrate the status register of 8251 for the given statement. Read status register and wait for DSR and TxRDY to become active. One active, get character from PC keyboard using INT B8H (keyboard routine).	Remember	8
2	Write an ALP for to transfer the data serially.	Understand	8
3	Identify the errors in receiving the data from asynchronous mode of operation using 8251.	Understand	8
4	Write the sequence of instructions required to initialize 8251 at address A0H and A1H for the configuration below. i)Character length – 8 bits	Understand	8
UNIT-V			

S. No	QUESTION	Blooms Taxonomy Level	Course Outcome
INTRODUCTION TO 8051 MICROCONTROLLERS			
PART-A (SHORT ANSWER QUESTIONS)			
1	Write the differences between microprocessor and microcontroller.	Remember	7
2	Discuss which ports of 8051 are bit addressable.	Understand	7
3	Explain why Port 0 needs pull-up resistors.	Understand	7
4	Write out of 128 bytes of RAM in the 8051, how many bytes are bit addressable? List them.	Remember	7
5	Explain how to save the status of P2.7 in RAM location 31.	Understand	7
6	List out the types of addressing modes in microcontroller.	Remember	7
7	Discus about I/O port expansion.	Understand	7
8	Explain how external interrupts are serviced in 8051	Understand	7
9	Discuss the flag pattern available in 8051.	Understand	7
10	Write the value of register A after each of the following instructions. MOV A,#26H	Remember	7
11	List out the on-chip resources available in the 8051 microcontroller?	Remember	7
12	Write the number of register banks in 8051 and say how the CPU knows which bank is currently in use.	Remember	7
13	Explain the controls signals to be used, if 8051 microcontroller demands interfacing of external memory.	Understand	7
14	Write the function of the bits PSW.3 & PSW.4.	Remember	7
15	Distinguish polling and interrupt mechanism. Find the value of the PSW register after the execution of the instructions.	Understand	7
16	Explain the use of SFR in 8051.	Understand	7
17	List all the SFRs involved in 8051.	Remember	7
18	Write the addressing modes supported by 8051.	Remember	7
PART-B (LONG ANSWER QUESTIONS)			
1	Discuss the register set of 8051 and also discuss how memory and I/O addressing is done in 8051.	Understand	7
2	Discuss internal architecture of 8051 microcontroller in detail.	Understand	7
3	Describe the five addressing modes of 8051 microcontroller with example.	Understand	7
4	Sketch and illustrate how to access external memory devices in an 8051 based system.	Understand	7

S. No	QUESTION	Blooms Taxonomy Level	Course Outcome
5	Discuss the internal memory organization of the 8051 microcontroller.	Understand	7
6	Design an 8051 based system with 16 K bytes of program ROM and 16 K bytes of data ROM.	Understand	7
7	Discuss about the memory organization and special function registers in 8051 microcontroller.	Understand	7
9	Describe the operation of I/O ports in 8051 with neat sketch.	Understand	7
10	Demonstrate the functioning of A & B registers of 8051.	Understand	7
11	List the format of PSW register of 8051 and explain each bit.	Remember	7
PART-C (ANALYTICAL QUESTIONS)			
1	Write 8051 program to convert packed BCD number available in accumulator, into two ASCII numbers and save them in internal RAM locations 48H and 49H.	Remember	7
2	Develop 8051 program to move a block of data from external program memory to external data memory.	Remember	7
3	Use PUSH instruction to put the number 82H in RAM locations 34H to 37H. also write same program without PUSH instruction.	Understand	7
4	Generate a program in 8051 to count number of zero bits available in a byte available in external RAM at 1000h. Store zero bit count in internal RAM location 60H.	Remember	7
5	Write 8051 program to OR the contents of port 1 and port 2, put the result in external RAM location 0102h.	Remember	7

Prepared By: Mr.R Mahendhar Reddy, Associate Professor,ECE

HOD, EEE